



REPUBLIC OF THE PHILIPPINES  
QUEZON CITY GOVERNMENT  
BIDS AND AWARDS COMMITTEE –  
GOODS AND SERVICES



# **PHILIPPINE BIDDING DOCUMENTS**

(As Harmonized with Development Partners)

## **PROCUREMENT OF INDUSTRIAL ELECTRONICS TRAINER (BASIC COMMUNICATION SYSTEM) AND OTHERS**

**PROJECT NO. QCU-23-ELTE-1295B**

Government of the Republic of the Philippines

**Sixth Edition  
July 2020**

# Preface

These Philippine Bidding Documents (PBDs) for the procurement of Goods through Competitive Bidding have been prepared by the Government of the Philippines for use by any branch, constitutional commission or office, agency, department, bureau, office, or instrumentality of the Government of the Philippines, National Government Agencies, including Government-Owned and/or Controlled Corporations, Government Financing Institutions, State Universities and Colleges, and Local Government Unit. The procedures and practices presented in this document have been developed through broad experience, and are for mandatory use in projects that are financed in whole or in part by the Government of the Philippines or any foreign government/foreign or international financing institution in accordance with the provisions of the 2016 revised Implementing Rules and Regulations of Republic Act No. 9184.

The Bidding Documents shall clearly and adequately define, among others: (i) the objectives, scope, and expected outputs and/or results of the proposed contract or Framework Agreement, as the case may be; (ii) the eligibility requirements of Bidders; (iii) the expected contract or Framework Agreement duration, the estimated quantity in the case of procurement of goods, delivery schedule and/or time frame; and (iv) the obligations, duties, and/or functions of the winning bidder.

Care should be taken to check the relevance of the provisions of the PBDs against the requirements of the specific Goods to be procured. If duplication of a subject is inevitable in other sections of the document prepared by the Procuring Entity, care must be exercised to avoid contradictions between clauses dealing with the same matter.

Moreover, each section is prepared with notes intended only as information for the Procuring Entity or the person drafting the Bidding Documents. They shall not be included in the final documents. The following general directions should be observed when using the documents:

- a. All the documents listed in the Table of Contents are normally required for the procurement of Goods. However, they should be adapted as necessary to the circumstances of the particular Procurement Project.
- b. Specific details, such as the “*name of the Procuring Entity*” and “*address for bid submission*,” should be furnished in the Instructions to Bidders, Bid Data Sheet, and Special Conditions of Contract. The final documents should contain neither blank spaces nor options.
- c. This Preface and the footnotes or notes in italics included in the Invitation to Bid, Bid Data Sheet, General Conditions of Contract, Special Conditions of Contract, Schedule of Requirements, and Specifications are not part of the text of the final document, although they contain instructions that the Procuring Entity should strictly follow.
- d. The cover should be modified as required to identify the Bidding Documents as to the Procurement Project, Project Identification Number, and Procuring Entity, in addition to the date of issue.

- e. Modifications for specific Procurement Project details should be provided in the Special Conditions of Contract as amendments to the Conditions of Contract. For easy completion, whenever reference has to be made to specific clauses in the Bid Data Sheet or Special Conditions of Contract, these terms shall be printed in bold typeface on Sections I (Instructions to Bidders) and III (General Conditions of Contract), respectively.
- f. For guidelines on the use of Bidding Forms and the procurement of Foreign-Assisted Projects, these will be covered by a separate issuance of the Government Procurement Policy Board.

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# ***Glossary of Acronyms, Terms, and Abbreviations***

**ABC** – Approved Budget for the Contract.

**BAC** – Bids and Awards Committee.

**Bid** – A signed offer or proposal to undertake a contract submitted by a bidder in response to and in consonance with the requirements of the bidding documents. Also referred to as *Proposal* and *Tender*. (2016 revised IRR, Section 5[c])

**Bidder** – Refers to a contractor, manufacturer, supplier, distributor and/or consultant who submits a bid in response to the requirements of the Bidding Documents. (2016 revised IRR, Section 5[d])

**Bidding Documents** – The documents issued by the Procuring Entity as the bases for bids, furnishing all information necessary for a prospective bidder to prepare a bid for the Goods, Infrastructure Projects, and/or Consulting Services required by the Procuring Entity. (2016 revised IRR, Section 5[e])

**BIR** – Bureau of Internal Revenue.

**BSP** – Bangko Sentral ng Pilipinas.

**Consulting Services** – Refer to services for Infrastructure Projects and other types of projects or activities of the GOP requiring adequate external technical and professional expertise that are beyond the capability and/or capacity of the GOP to undertake such as, but not limited to: (i) advisory and review services; (ii) pre-investment or feasibility studies; (iii) design; (iv) construction supervision; (v) management and related services; and (vi) other technical services or special studies. (2016 revised IRR, Section 5[i])

**CDA** - Cooperative Development Authority.

**Contract** – Refers to the agreement entered into between the Procuring Entity and the Supplier or Manufacturer or Distributor or Service Provider for procurement of Goods and Services; Contractor for Procurement of Infrastructure Projects; or Consultant or Consulting Firm for Procurement of Consulting Services; as the case may be, as recorded in the Contract Form signed by the parties, including all attachments and appendices thereto and all documents incorporated by reference therein.

**CIF** – Cost Insurance and Freight.

**CIP** – Carriage and Insurance Paid.

**CPI** – Consumer Price Index.

**DDP** – Refers to the quoted price of the Goods, which means “delivered duty paid.”

**DTI** – Department of Trade and Industry.

**EXW** – Ex works.

**FCA** – “Free Carrier” shipping point.

**FOB** – “Free on Board” shipping point.

**Foreign-funded Procurement or Foreign-Assisted Project**– Refers to procurement whose funding source is from a foreign government, foreign or international financing institution as specified in the Treaty or International or Executive Agreement. (2016 revised IRR, Section 5[b]).

**Framework Agreement** – Refers to a written agreement between a procuring entity and a supplier or service provider that identifies the terms and conditions, under which specific purchases, otherwise known as “Call-Offs,” are made for the duration of the agreement. It is in the nature of an option contract between the procuring entity and the bidder(s) granting the procuring entity the option to either place an order for any of the goods or services identified in the Framework Agreement List or not buy at all, within a minimum period of one (1) year to a maximum period of three (3) years. (GPPB Resolution No. 27-2019)

**GFI** – Government Financial Institution.

**GOCC** – Government-owned and/or –controlled corporation.

**Goods** – Refer to all items, supplies, materials and general support services, except Consulting Services and Infrastructure Projects, which may be needed in the transaction of public businesses or in the pursuit of any government undertaking, project or activity, whether in the nature of equipment, furniture, stationery, materials for construction, or personal property of any kind, including non-personal or contractual services such as the repair and maintenance of equipment and furniture, as well as trucking, hauling, janitorial, security, and related or analogous services, as well as procurement of materials and supplies provided by the Procuring Entity for such services. The term “related” or “analogous services” shall include, but is not limited to, lease or purchase of office space, media advertisements, health maintenance services, and other services essential to the operation of the Procuring Entity. (2016 revised IRR, Section 5[r])

**GOP** – Government of the Philippines.

**GPPB** – Government Procurement Policy Board.

**INCOTERMS** – International Commercial Terms.

**Infrastructure Projects** – Include the construction, improvement, rehabilitation, demolition, repair, restoration or maintenance of roads and bridges, railways, airports, seaports, communication facilities, civil works components of information technology projects, irrigation, flood control and drainage, water supply, sanitation, sewerage and solid waste management systems, shore protection, energy/power and electrification facilities, national buildings, school buildings, hospital buildings, and other related construction projects of the government. Also referred to as *civil works or works*. (2016 revised IRR, Section 5[u])

**LGUs** – Local Government Units.

**NFCC** – Net Financial Contracting Capacity.

**NGA** – National Government Agency.

**PhilGEPS** - Philippine Government Electronic Procurement System.

**Procurement Project** – refers to a specific or identified procurement covering goods, infrastructure project or consulting services. A Procurement Project shall be described, detailed, and scheduled in the Project Procurement Management Plan prepared by the agency

which shall be consolidated in the procuring entity's Annual Procurement Plan. (GPPB Circular No. 06-2019 dated 17 July 2019)

**PSA** – Philippine Statistics Authority.

**SEC** – Securities and Exchange Commission.

**SLCC** – Single Largest Completed Contract.

**Supplier** – refers to a citizen, or any corporate body or commercial company duly organized and registered under the laws where it is established, habitually established in business and engaged in the manufacture or sale of the merchandise or performance of the general services covered by his bid. (Item 3.8 of GPPB Resolution No. 13-2019, dated 23 May 2019). Supplier as used in these Bidding Documents may likewise refer to a distributor, manufacturer, contractor, or consultant.

**UN** – United Nations.

## ***Section I. Invitation to Bid***

### **Notes on the Invitation to Bid**

The Invitation to Bid (IB) provides information that enables potential Bidders to decide whether to participate in the procurement at hand. The IB shall be posted in accordance with Section 21.2 of the 2016 revised IRR of RA No. 9184.

Apart from the essential items listed in the Bidding Documents, the IB should also indicate the following:

- a. The date of availability of the Bidding Documents, which shall be from the time the IB is first advertised/posted until the deadline for the submission and receipt of bids;
- b. The place where the Bidding Documents may be acquired or the website where it may be downloaded;
- c. The deadline for the submission and receipt of bids; and
- d. Any important bid evaluation criteria (*e.g.*, the application of a margin of preference in bid evaluation).

The IB should be incorporated in the Bidding Documents. The information contained in the IB must conform to the Bidding Documents and in particular to the relevant information in the Bid Data Sheet.





QUEZON CITY GOVERNMENT  
BAC – GOODS AND SERVICES



INVITATION TO BID

October 05, 2023

	PROJECT NO.	OFFICE	PROJECT NAME	AMOUNT	SOURCE OF FUND	DELIVERY PERIOD
1.	CAO(TFS)-23-VRM-1156	CITY ADMINISTRATOR'S OFFICE (TASK FORCE STREETLIGHTS)	RM - MOTOR VEHICLES & HEAVY EQUIPMENT (PARTS AND LABOR)	P 1,121,774.00	GENERAL FUND	60 CD
2.	CAO(CPDD)-23-GM-1507B	CITY ADMINISTRATOR'S OFFICE (CPDD)	CELLPHONE PREPAID CARDS	2,074,000.00	GENERAL FUND	30 CD
3.	ENGINEERING-23-HCS-1622	DEPARTMENT OF ENGINEERING	LINE 1: VARIOUS HARDWARE SUPPLIES	P 1,629,013.98	GENERAL FUND	45 CD
			LINE 2: VARIOUS HARDWARE SUPPLIES	P 4,135,284.33	GENERAL FUND	45 CD
4.	OCM-23-ESLC-1613	OFFICE OF THE CITY MAYOR	LINE 1: SUPPLY, DELIVERY, INSTALLATION/DISMANTLING AND TESTING OF NEW LAMPOST LANTERN	P 16,965,780.00	GENERAL FUND	1 MONTH
			LINE 2: SUPPLY, DELIVERY, INSTALLATION/DISMANTLING AND TESTING OF NEW LAMPOST LANTERN	P 14,039,400.00	GENERAL FUND	1 MONTH
5.	CONSO-23-EM-1629	OFFICE OF THE CITY MAYOR	LINE 1: EVENTS MANAGEMENT FOR VARIOUS ACTIVITIES	P 11,387,840.00	GENERAL FUND	1 MONTH
			LINE 2: EVENTS MANAGEMENT FOR VARIOUS ACTIVITIES	P 4,144,366.00	GENERAL FUND	1 MONTH
6.	OCM-23-TFLES-1445B	OFFICE OF THE CITY MAYOR	HOTEL ACCOMMODATION AND OTHERS	P 3,372,000.00	GENERAL FUND	1 MONTH
7.	OCM-23-VPS2-1432B	OFFICE OF THE CITY MAYOR	VIDEO PRODUCTION PACKAGE (SANDY IN THE CITY)	P 3,000,000.00	GENERAL FUND	1 MONTH
8.	OCM(POPS)-23-VEHICLES-1491	OFFICE OF THE CITY MAYOR - POPS PLAN	MULTI CAB	P 1,482,000.00	GENERAL FUND	30 CD
9.	OCM(POPS)-23-VEHICLES-1492	OFFICE OF THE CITY MAYOR - POPS PLAN	MINI DUMP TRUCK	P 2,145,000.00	GENERAL FUND	30 CD
10.	OCM(GAD)-23-CS1-1439B	OFFICE OF THE CITY MAYOR (GAD)	FOOD AND DRINKS	P 8,100,000.00	GENERAL FUND	1 MONTH
11.	CONSO-23-OE-1652	OFFICE OF THE VICE MAYOR	ID CARD PRINTER, PHOTOCOPIER MACHINE AND OTHERS	P 1,407,387.00	GENERAL FUND	30 CD
12.	PAISD-23-VEHICLES-0582	PUBLIC AFFAIRS AND INFORMATION SERVICES DEPARTMENT	MOTOR VEHICLE (MULTI-PURPOSE VEHICLE)	P 1,800,000.00	GENERAL FUND	30 CD
13.	QCADAAC-23-VEHICLES-1545	QUEZON CITY ANTI-DRUG ABUSE ADVISORY COUNCIL	MOTOR VEHICLE	P 1,250,000.00	GENERAL FUND	60 CD
14.	CONSO-23-CS1-1513B	QUEZON CITY DISASTER RISK REDUCTION AND MANAGEMENT OFFICE	LINE 1: FOOD AND DRINKS AND OTHERS	P 4,488,221.28	GENERAL FUND	1 MONTH
			LINE 2: FOOD AND DRINKS AND OTHERS	P 1,999,990.00		

15.	CONSO-23-HLMF-1562B	QUEZON CITY DISASTER RISK REDUCTION AND MANAGEMENT OFFICE	LINE 2: HOTEL ACCOMMODATION AND OTHERS	P 16,178,800.00	GENERAL FUND	1 MONTH
16.	QCGH-23-OESC-1462B	QUEZON CITY GENERAL HOSPITAL	LINE 1: COPY PRINTER INK AND MASTER ROLL	P 1,362,960.00	GENERAL FUND	30 CD
			LINE 2: INKS AND TONERS	P 1,421,915.65	GENERAL FUND	30 CD
17.	QCGH-23-OSD-1431B	QUEZON CITY GENERAL HOSPITAL	VARIOUS OFFICE SUPPLIES	P 1,398,198.25	GENERAL FUND	30 CD
18.	HEALTH-23-HME-0551B	QUEZON CITY HEALTH DEPARTMENT	AUTOMATIC AUTOCLAVE AND OTHERS	P 1,786,673.83	GENERAL FUND	30 CD
19.	QCU-23-ELTE-1295B	QUEZON CITY UNIVERSITY	INDUSTRIAL ELECTRONICS TRAINER (BASIC COMMUNICATION SYSTEM) AND OTHERS	P 14,095,125.00	GENERAL FUND	120 CD
20.	QCU-23-PS2-1485B	QUEZON CITY UNIVERSITY	PRINTING OF ANNUAL REPORT AND OTHERS	P 1,842,150.00	GENERAL FUND	30 CD
21.	CONSO-23-PS2-1277B	SCHOOLS DIVISION OFFICE	PRINTING OF DIVISION READING ASSESSMENT TOOLS AND OTHERS	P 1,258,244.00	SPECIAL EDUCATION FUND	30 CD
22.	SDO-23-DM-1547	SCHOOLS DIVISION OFFICE	DRUGS AND MEDICINES FOR DENTAL	P 1,051,725.00	SPECIAL EDUCATION FUND	30 CD
23.	SDO-23-PP-1448B	SCHOOLS DIVISION OFFICE	MONOBLOC CHAIRS	P 39,600,000.00	SPECIAL EDUCATION FUND	30 CD
24.	CONSO-23-EMS-1458B	SCHOOLS DIVISION OFFICE	LINE 2: LETTER CARDS AND OTHERS	P 3,111,000.00	SPECIAL EDUCATION FUND	30 CD
25.	SDO-23-PS2-1553B	SCHOOLS DIVISION OFFICE	LINE 2: ALS MODULES	P 3,198,320.00	SPECIAL EDUCATION FUND	30 CD

1. The **QUEZON CITY LOCAL GOVERNMENT**, through the *General Fund and Special Education Fund*, of various years intends to apply the sums stated above being the ABC to payments under the contract for the above stated projects of contract for each lot/item. Bids received in excess of the ABC shall be automatically rejected at bid opening.
2. The **QUEZON CITY LOCAL GOVERNMENT** now invites bids for various **Projects**. Delivery of the Goods is required *as stated above*. Bidders should have completed, within **the last three (3) years** from the date of submission and receipt of bids, a contract similar to the Project. The description of an eligible bidder is contained in the Bidding Documents, particularly, in Section II. Instructions to Bidders.
3. Bidding will be conducted through open competitive bidding procedures using a non-discretionary “*pass/fail*” criterion as specified in the 2016 revised Implementing Rules and Regulations (IRR) of Republic Act (RA) No. 9184.
  - a. Bidding is restricted to Filipino citizens/sole proprietorships, partnerships, or organizations with at least sixty percent (60%) interest or outstanding capital stock belonging to citizens of the Philippines, and to citizens or organizations of a country the laws or regulations of which grant similar rights or privileges to Filipino citizens, pursuant to RA No. 5183.
4. Prospective Bidders may obtain further information from **QUEZON CITY GOVERNMENT Bids and Awards Committee (BAC) Secretariat** and inspect the Bidding Documents at the address given below during *weekdays from 8:00 a.m. – 5:00 p.m.*
5. A complete set of Bidding Documents may be acquired by interested Bidders on **Friday, October 06, 2023** from the given address and website(s) below *and upon payment of the applicable fee for the Bidding Documents, pursuant to the latest Guidelines issued by the GPPB*. The Procuring Entity shall allow the bidder to present its proof of payment for the fees *in person*.

STANDARD RATES:

Approved Budget for the Contract	Maximum Cost of Bidding Documents (in Philippine Peso)
500,000 and below	500.00
More than 500,000 up to 1 Million	1,000.00
More than 1 Million up to 5 Million	5,000.00
More than 5 Million up to 10 Million	10,000.00
More than 10 Million up to 50 Million	25,000.00
More than 50 Million up to 500 Million	50,000.00
More than 500 Million	75,000.00

The following are the requirements for purchase of Bidding Documents;

- PhilGEPS Registration Certificate (Platinum – 3 pages)
  - Document Request List (DRL)
  - Authorization to Purchase Bidding Documents
    - Corporate Secretary Certificate for corporation (specific for the project)
    - Special Power of Attorney for single proprietorship (specific for the project)
  - Notarized Joint Venture Agreement (as applicable)
6. The **Quezon City Local Government** will hold a Pre-Bid Conference on 10:30 A.M. of **Friday, October 13, 2023** at **2<sup>nd</sup> Floor, Procurement Department-Bidding Room, Finance Building, Quezon City Hall Compound**, and/or through video conferencing *via Zoom* which shall be open to prospective bidders.

Topic: BAC-GOODS Pre-Bid Conference Meeting

Join Zoom Meeting

<https://us02web.zoom.us/j/84835002246?pwd=OVRuVE0weXZMNXYwZG5LaWd1dXk1QT09>

Meeting ID: 848 3500 2246

Passcode: 154733

7. Bids must be duly received by the BAC Secretariat through manual submission at the 2<sup>nd</sup> Floor, Procurement Department, Finance Building, Quezon City Hall Compound on or before **10:00 A.M.** of **Friday, October 27, 2023**. Late bids shall not be accepted.
8. All Bids must be accompanied by a bid security in any of the acceptable forms and in the amount stated in **ITB** Clause 14.
9. Bid opening shall be on **11:00 A.M. of Friday, October 27, 2023** at the given address below and/or via Zoom. Bids will be opened in the presence of the bidders’ representatives who choose to attend the activity.

Topic: BAC-GOODS & SERVICES BIDDING

Join Zoom Meeting

<https://us02web.zoom.us/j/85850855933?pwd=R2dZUUp4Z3lyU29iZGVlWmdKRjZCdz09>

Meeting ID: 858 5085 5933

Passcode: 118682

10. The **Quezon City Local Government** reserves the right to reject any and all bids, declare a failure of bidding, or not award the contract at any time prior to contract award in accordance with Sections 35.6 and 41 of the 2016 revised IRR of RA No. 9184, without thereby incurring any liability to the affected bidder or bidders.
11. For further information, please refer to:

**ATTY. DOMINIC B. GARCIA**

OIC, Procurement Department

2<sup>nd</sup> Floor, Procurement Department,

Finance Building, Quezon City Hall Compound

Elliptical Road, Barangay Central Diliman, Quezon City.

Email Add: [bacgoods.procurement@quezoncity.gov.ph](mailto:bacgoods.procurement@quezoncity.gov.ph)

Tel. No. (02)8988-4242 loc. 8506/8710

Website: [www.quezoncity.gov.ph](http://www.quezoncity.gov.ph)

12. You may visit the following websites:

For downloading of Bidding Documents: [www.quezoncity.gov.ph](http://www.quezoncity.gov.ph)

By:

(SGD.) **MS. MA. MARGARITA T. SANTOS, DPA**  
Chairperson, QC-BAC-Goods and Services

# *Section II. Instructions to Bidders*

## **Notes on the Instructions to Bidders**

This Section on the Instruction to Bidders (ITB) provides the information necessary for bidders to prepare responsive bids, in accordance with the requirements of the Procuring Entity. It also provides information on bid submission, eligibility check, opening and evaluation of bids, post-qualification, and on the award of contract.

## 1. Scope of Bid

The Procuring Entity, **Quezon City Local Government** wishes to receive Bids for the **PROCUREMENT OF INDUSTRIAL ELECTRONICS TRAINER (BASIC COMMUNICATION SYSTEM) AND OTHERS** with identification number **QCU-23-ELTE-1295B**.

[Note: The Project Identification Number is assigned by the Procuring Entity based on its own coding scheme and is not the same as the PhilGEPS reference number, which is generated after the posting of the bid opportunity on the PhilGEPS website.]

The Procurement Project (referred to herein as “Project”) is composed of **Seven (7) items**, the details of which are described in Section VII (Technical Specifications).

## 2. Funding Information

2.1. The GOP through the source of funding as indicated below for **2023** in the amount of **FOURTEEN MILLION NINETY FIVE THOUSAND ONE HUNDRED TWENTY FIVE PESOS AND 00/100 ONLY (Php14,095,125.00)**.

2.2. The source of funding is:

*a.* LGUs, the Annual or Supplemental Budget, as approved by the Sanggunian.

## 3. Bidding Requirements

The Bidding for the Project shall be governed by all the provisions of RA No. 9184 and its 2016 revised IRR, including its Generic Procurement Manuals and associated policies, rules and regulations as the primary source thereof, while the herein clauses shall serve as the secondary source thereof.

Any amendments made to the IRR and other GPPB issuances shall be applicable only to the ongoing posting, advertisement, or **IB** by the BAC through the issuance of a supplemental or bid bulletin.

The Bidder, by the act of submitting its Bid, shall be deemed to have verified and accepted the general requirements of this Project, including other factors that may affect the cost, duration and execution or implementation of the contract, project, or work and examine all instructions, forms, terms, and project requirements in the Bidding Documents.

## 4. Corrupt, Fraudulent, Collusive, and Coercive Practices

The Procuring Entity, as well as the Bidders and Suppliers, shall observe the highest standard of ethics during the procurement and execution of the contract. They or through an agent shall not engage in corrupt, fraudulent, collusive, coercive, and obstructive practices defined under Annex “I” of the 2016 revised IRR of RA No. 9184 or other integrity violations in competing for the Project.

## 5. Eligible Bidders

5.1. Only Bids of Bidders found to be legally, technically, and financially capable will be evaluated.

- 5.2. Foreign ownership exceeding those allowed under the rules may participate pursuant to:
- i. When a Treaty or International or Executive Agreement as provided in Section 4 of the RA No. 9184 and its 2016 revised IRR allow foreign bidders to participate;
  - ii. Citizens, corporations, or associations of a country, included in the list issued by the GPPB, the laws or regulations of which grant reciprocal rights or privileges to citizens, corporations, or associations of the Philippines;
  - iii. When the Goods sought to be procured are not available from local suppliers; or
  - iv. When there is a need to prevent situations that defeat competition or restrain trade.
- 5.3. Pursuant to Section 23.4.1.3 of the 2016 revised IRR of RA No.9184, the Bidder shall have an SLCC that is at least one (1) contract similar to the Project the value of which, adjusted to current prices using the PSA's CPI, must be at least equivalent to:
- a. For the procurement **Non-Expendable Supplies and Services**: The Bidder must have completed a single contract that is similar to this Project, equivalent to at least **fifty percent (50%)** of the ABC.
- 5.4. The Bidders shall comply with the eligibility criteria under Section 23.4.1 of the 2016 IRR of RA No. 9184.

## 6. Origin of Goods

There is no restriction on the origin of goods other than those prohibited by a decision of the UN Security Council taken under Chapter VII of the Charter of the UN, subject to Domestic Preference requirements under **ITB** Clause 18.

## 7. Subcontracts

- 7.1. The Bidder may subcontract portions of the Project to the extent allowed by the Procuring Entity as stated herein, but in no case more than twenty percent (20%) of the Project.

The Procuring Entity has prescribed that: Subcontracting is not allowed.

## 8. Pre-Bid Conference

The Procuring Entity will hold a pre-bid conference for this Project on the specified date and time and either at its physical address and/or through videoconferencing as indicated in paragraph 6 of the **IB**.

## 9. Clarification and Amendment of Bidding Documents

Prospective bidders may request for clarification on and/or interpretation of any part of the Bidding Documents. Such requests must be in writing and received by the

Procuring Entity, either at its given address or through electronic mail indicated in the **IB**, at least ten (10) calendar days before the deadline set for the submission and receipt of Bids.

## **10. Documents comprising the Bid: Eligibility and Technical Components**

- 10.1. The first envelope shall contain the eligibility and technical documents of the Bid as specified in **Section VIII (Checklist of Technical and Financial Documents)**.
- 10.2. The Bidder's SLCC as indicated in **ITB** Clause 5.3 should have been completed within *the last three (3) years* prior to the deadline for the submission and receipt of bids.
- 10.3. If the eligibility requirements or statements, the bids, and all other documents for submission to the BAC are in foreign language other than English, it must be accompanied by a translation in English, which shall be authenticated by the appropriate Philippine foreign service establishment, post, or the equivalent office having jurisdiction over the foreign bidder's affairs in the Philippines. Similar to the required authentication above, for Contracting Parties to the Apostille Convention, only the translated documents shall be authenticated through an Apostille pursuant to GPPB Resolution No. 13-2019 dated 23 May 2019. The English translation shall govern, for purposes of interpretation of the bid.

## **11. Documents comprising the Bid: Financial Component**

- 11.1. The second bid envelope shall contain the financial documents for the Bid as specified in **Section VIII (Checklist of Technical and Financial Documents)**.
- 11.2. If the Bidder claims preference as a Domestic Bidder or Domestic Entity, a certification issued by DTI shall be provided by the Bidder in accordance with Section 43.1.3 of the 2016 revised IRR of RA No. 9184.
- 11.3. Any bid exceeding the ABC indicated in paragraph 1 of the **IB** shall not be accepted.
- 11.4. For Foreign-funded Procurement, a ceiling may be applied to bid prices provided the conditions are met under Section 31.2 of the 2016 revised IRR of RA No. 9184.

## **12. Bid Prices**

- 12.1. Prices indicated on the Price Schedule shall be entered separately in the following manner:
  - a. For Goods offered from within the Procuring Entity's country:
    - i. The price of the Goods quoted EXW (ex-works, ex-factory, ex-warehouse, ex-showroom, or off-the-shelf, as applicable);
    - ii. The cost of all customs duties and sales and other taxes already paid or payable;
    - iii. The cost of transportation, insurance, and other costs incidental to delivery of the Goods to their final destination; and

- iv. The price of other (incidental) services, if any, listed in e.
- b. For Goods offered from abroad:
  - i. Unless otherwise stated in the **BDS**, the price of the Goods shall be quoted delivered duty paid (DDP) with the place of destination in the Philippines as specified in the **BDS**. In quoting the price, the Bidder shall be free to use transportation through carriers registered in any eligible country. Similarly, the Bidder may obtain insurance services from any eligible source country.
  - ii. The price of other (incidental) services, if any, as listed in **Section VII (Technical Specifications)**.

### 13. Bid and Payment Currencies

- 13.1. For Goods that the Bidder will supply from outside the Philippines, the bid prices may be quoted in the local currency or tradeable currency accepted by the BSP at the discretion of the Bidder. However, for purposes of bid evaluation, Bids denominated in foreign currencies, shall be converted to Philippine currency based on the exchange rate as published in the BSP reference rate bulletin on the day of the bid opening.
- 13.2. Payment of the contract price shall be made in:
  - a. Philippine Pesos.

### 14. Bid Security

- 14.1. The Bidder shall submit a Bid Securing Declaration<sup>1</sup> or any form of Bid Security in the amount indicated in the **BDS**, which shall be not less than the percentage of the ABC in accordance with the schedule in the **BDS**.
- 14.2. The Bid and bid security *in no case shall exceed One Hundred Twenty (120) calendar days from the date of opening of bids, unless duly extended by the bidder upon the request of the Head of the Procuring Entity (HoPE) of the Quezon City Local Government*. Any Bid not accompanied by an acceptable bid security shall be rejected by the Procuring Entity as non-responsive.

### 15. Sealing and Marking of Bids

Each Bidder shall submit one copy of the first and second components of its Bid.

The Procuring Entity may request additional hard copies and/or electronic copies of the Bid. However, failure of the Bidders to comply with the said request shall not be a ground for disqualification.

If the Procuring Entity allows the submission of bids through online submission or any other electronic means, the Bidder shall submit an electronic copy of its Bid, which must be digitally signed. An electronic copy that cannot be opened or is corrupted shall be considered non-responsive and, thus, automatically disqualified.

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<sup>1</sup> In the case of Framework Agreement, the undertaking shall refer to entering into contract with the Procuring Entity and furnishing of the performance security or the performance securing declaration within ten (10) calendar days from receipt of Notice to Execute Framework Agreement.



## 16. Deadline for Submission of Bids

- 16.1. The Bidders shall submit on the specified date and time through manual submission as indicated in paragraph 7 of the **IB**.

## 17. Opening and Preliminary Examination of Bids

- 17.1. The BAC shall open the Bids in public at the time, on the date, and at the place specified in paragraph 9 of the **IB**. The Bidders' representatives who are present shall sign a register evidencing their attendance. In case videoconferencing, webcasting or other similar technologies will be used, attendance of participants shall likewise be recorded by the BAC Secretariat.

In case the Bids cannot be opened as scheduled due to justifiable reasons, the rescheduling requirements under Section 29 of the 2016 revised IRR of RA No. 9184 shall prevail.

- 17.2. The preliminary examination of bids shall be governed by Section 30 of the 2016 revised IRR of RA No. 9184.

## 18. Domestic Preference

- 18.1. The Procuring Entity will grant a margin of preference for the purpose of comparison of Bids in accordance with Section 43.1.2 of the 2016 revised IRR of RA No. 9184.

## 19. Detailed Evaluation and Comparison of Bids

- 19.1. The Procuring BAC shall immediately conduct a detailed evaluation of all Bids rated "*passed*," using non-discretionary pass/fail criteria. The BAC shall consider the conditions in the evaluation of Bids under Section 32.2 of the 2016 revised IRR of RA No. 9184.

- 19.2. If the Project allows partial bids, bidders may submit a proposal on any of the lots or items, and evaluation will be undertaken on a per lot or item basis, as the case maybe. In this case, the Bid Security as required by **ITB** Clause 15 shall be submitted for each lot or item separately.

- 19.3. The descriptions of the lots or items shall be indicated in **Section VII (Technical Specifications)**, although the ABCs of these lots or items are indicated in the **BDS** for purposes of the NFCC computation pursuant to Section 23.4.2.6 of the 2016 revised IRR of RA No. 9184. The NFCC must be sufficient for the total of the ABCs for all the lots or items participated in by the prospective Bidder.

- 19.4. The Project shall be awarded as follows:

One Project having several items that shall be awarded as one contract.

- 19.5. Except for bidders submitting a committed Line of Credit from a Universal or Commercial Bank in lieu of its NFCC computation, all Bids must include the NFCC computation pursuant to Section 23.4.1.4 of the 2016 revised IRR of RA No. 9184, which must be sufficient for the total of the ABCs for all the lots or items participated in by the prospective Bidder. For bidders submitting the committed Line of Credit, it must be at least equal to ten percent (10%) of the ABCs for all the lots or items participated in by the prospective Bidder.

## **20. Post-Qualification**

- 20.1. Within a non-extendible period of five (5) calendar days from receipt by the Bidder of the notice from the BAC that it submitted the Lowest Calculated Bid, the Bidder shall submit its latest income and business tax returns filed and paid through the BIR Electronic Filing and Payment System (eFPS) and other appropriate licenses and permits required by law and stated in the **BDS**.

## **21. Signing of the Contract**

- 21.1. The documents required in Section 37.2 of the 2016 revised IRR of RA No. 9184 shall form part of the Contract. Additional Contract documents are indicated in the **BDS**.

## ***Section III. Bid Data Sheet***

### **Notes on the Bid Data Sheet**

The Bid Data Sheet (BDS) consists of provisions that supplement, amend, or specify in detail, information, or requirements included in the ITB found in Section II, which are specific to each procurement.

This Section is intended to assist the Procuring Entity in providing the specific information in relation to corresponding clauses in the ITB and has to be prepared for each specific procurement.

The Procuring Entity should specify in the BDS information and requirements specific to the circumstances of the Procuring Entity, the processing of the procurement, and the bid evaluation criteria that will apply to the Bids. In preparing the BDS, the following aspects should be checked:

- a. Information that specifies and complements provisions of the ITB must be incorporated.
- b. Amendments and/or supplements, if any, to provisions of the ITB as necessitated by the circumstances of the specific procurement, must also be incorporated.

# Bid Data Sheet

ITB Clause	
5.3	<p>For this purpose, contracts similar to the Project shall be:</p> <ol style="list-style-type: none"> <li><i>A single contract similar to <b>the item/s to be bid</b> and must be at least <b>fifty percent (50%)</b> of the ABC.</i></li> <li>Completed within the last three (3) years prior to the deadline for the submission and receipt of bids substantially in a <b>FORM prescribed by the QC-BAC-GOODS AND SERVICES, must be accompanied by a copy of Certificate of Acceptance by the end-user or Official Receipt (O.R) or Sales Invoice (S.I.) issued for the Contract.</b></li> </ol>
7.1	Subcontracting is not allowed.
12	The price of the Goods shall be quoted DDP <i>within Quezon City</i> or the applicable International Commercial Terms (INCOTERMS) for this Project.
14.1	<p>The bid security shall be in the form of a Bid Securing Declaration, or any of the following forms and amounts:</p> <ol style="list-style-type: none"> <li>The amount of not less than <b>Php281,902.50</b> or equivalent to <b>two percent (2%)</b> of ABC if bid security is in cash, cashier's/manager's check, bank draft/guarantee or irrevocable letter of credit; or</li> <li>The amount of not less than <b>Php704,756.25</b> or equivalent to <b>five percent (5%)</b> of ABC if bid security is in Surety Bond.</li> </ol>
19.3	<p><i>[In case the Project will be awarded by lot, list the grouping of lots by specifying the group title, items, and the quantity for every identified lot, and the corresponding ABC for each lot.]</i></p> <p><i>[In case the project will be awarded by item, list each item indicating its quantity and ABC.]</i></p>
20.2	<p>List of required licenses and permits relevant to the Project and the corresponding law requiring it.</p> <ul style="list-style-type: none"> <li><b>No additional requirement.</b></li> </ul>
21.2	<p>Additional required documents relevant to the Project that are required by existing laws and/or the Procuring Entity.</p> <ul style="list-style-type: none"> <li>Attach proof of ISO certification to ensure that the quality of products and services complies with industry and international standards.</li> <li>Bidder must submit brochure catalog indicating the brand name and model of bid items.</li> <li>Bidder must attach Trainor's certification from the manufacturer to assure the quality of training that will be provided.</li> <li>Notarized Affidavit of Undertaking stating compliance to the following: <ul style="list-style-type: none"> <li>Bidder must conduct after sales training for the end-user after the completion of delivery for 18 Engineering faculty members and Laboratory personnel for 5 days</li> <li>Supplier must provide continuous technical support to equipment parts including services and repair as long as the equipment is functional.</li> <li>Equipment must be supplied with training manuals in English.</li> </ul> </li> </ul>

	<ul style="list-style-type: none"><li>• Statement of Warranty – minimum of one (1) year for all items</li><li>• Statement of After Sales Service<ul style="list-style-type: none"><li>➤ Free trainer's training</li><li>➤ Free Semi-Annual Re-training with certificate, on request.</li><li>➤ Free Consultation/Seminar, on request</li><li>➤ Free Annual product inspection, on request</li><li>➤ Free Product demonstration, on Request</li></ul></li></ul>
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# *Section IV. General Conditions of Contract*

## **Notes on the General Conditions of Contract**

The General Conditions of Contract (GCC) in this Section, read in conjunction with the Special Conditions of Contract in Section V and other documents listed therein, should be a complete document expressing all the rights and obligations of the parties.

Matters governing performance of the Supplier, payments under the contract, or matters affecting the risks, rights, and obligations of the parties under the contract are included in the GCC and Special Conditions of Contract.

Any complementary information, which may be needed, shall be introduced only through the Special Conditions of Contract.

## 1. Scope of Contract

This Contract shall include all such items, although not specifically mentioned, that can be reasonably inferred as being required for its completion as if such items were expressly mentioned herein. All the provisions of RA No. 9184 and its 2016 revised IRR, including the Generic Procurement Manual, and associated issuances, constitute the primary source for the terms and conditions of the Contract, and thus, applicable in contract implementation. Herein clauses shall serve as the secondary source for the terms and conditions of the Contract.

This is without prejudice to Sections 74.1 and 74.2 of the 2016 revised IRR of RA No. 9184 allowing the GPPB to amend the IRR, which shall be applied to all procurement activities, the advertisement, posting, or invitation of which were issued after the effectivity of the said amendment.

Additional requirements for the completion of this Contract shall be provided in the **Special Conditions of Contract (SCC)**.

## 2. Advance Payment and Terms of Payment

2.1. Advance payment of the contract amount is provided under Annex “D” of the revised 2016 IRR of RA No. 9184.

2.2. The Procuring Entity is allowed to determine the terms of payment on the partial or staggered delivery of the Goods procured, provided such partial payment shall correspond to the value of the goods delivered and accepted in accordance with prevailing accounting and auditing rules and regulations. The terms of payment are indicated in the **SCC**.

## 3. Performance Security

Within ten (10) calendar days from receipt of the Notice of Award by the Bidder from the Procuring Entity but in no case later than prior to the signing of the Contract by both parties, the successful Bidder shall furnish the performance security in any of the forms prescribed in Section 39 of the 2016 revised IRR of RA No. 9184.

## 4. Inspection and Tests

The Procuring Entity or its representative shall have the right to inspect and/or to test the Goods to confirm their conformity to the Project specifications at no extra cost to the Procuring Entity in accordance with the Generic Procurement Manual. In addition to tests in the **SCC, Section IV (Technical Specifications)** shall specify what inspections and/or tests the Procuring Entity requires, and where they are to be conducted. The Procuring Entity shall notify the Supplier in writing, in a timely manner, of the identity of any representatives retained for these purposes.

All reasonable facilities and assistance for the inspection and testing of Goods, including access to drawings and production data, shall be provided by the Supplier to the authorized inspectors at no charge to the Procuring Entity.

## 5. Warranty

6.1. In order to assure that manufacturing defects shall be corrected by the Supplier, a warranty shall be required from the Supplier as provided under Section 62.1 of the 2016 revised IRR of RA No. 9184.

- 6.2. The Procuring Entity shall promptly notify the Supplier in writing of any claims arising under this warranty. Upon receipt of such notice, the Supplier shall, repair or replace the defective Goods or parts thereof without cost to the Procuring Entity, pursuant to the Generic Procurement Manual.

## **6. Liability of the Supplier**

The Supplier's liability under this Contract shall be as provided by the laws of the Republic of the Philippines.

If the Supplier is a joint venture, all partners to the joint venture shall be jointly and severally liable to the Procuring Entity.



# *Section V. Special Conditions of Contract*

## **Notes on the Special Conditions of Contract**

Similar to the BDS, the clauses in this Section are intended to assist the Procuring Entity in providing contract-specific information in relation to corresponding clauses in the GCC found in Section IV.

The Special Conditions of Contract (SCC) complement the GCC, specifying contractual requirements linked to the special circumstances of the Procuring Entity, the Procuring Entity’s country, the sector, and the Goods purchased. In preparing this Section, the following aspects should be checked:

- a. Information that complements provisions of the GCC must be incorporated.
- b. Amendments and/or supplements to provisions of the GCC as necessitated by the circumstances of the specific purchase, must also be incorporated.

However, no special condition which defeats or negates the general intent and purpose of the provisions of the GCC should be incorporated herein.

# Special Conditions of Contract

GCC Clause	
<p>1</p>	<p><i>[List here any additional requirements for the completion of this Contract. The following requirements and the corresponding provisions may be deleted, amended, or retained depending on its applicability to this Contract:]</i></p> <p><b>Delivery and Documents –</b></p> <p>For purposes of the Contract, “EXW,” “FOB,” “FCA,” “CIF,” “CIP,” “DDP” and other trade terms used to describe the obligations of the parties shall have the meanings assigned to them by the current edition of INCOTERMS published by the International Chamber of Commerce, Paris. The Delivery terms of this Contract shall be as follows:</p> <p><i>[For Goods supplied from abroad, state:]</i> “The delivery terms applicable to the Contract are DDP delivered <i>[indicate place of destination]</i>. In accordance with INCOTERMS.”</p> <p><i>[For Goods supplied from within the Philippines, state:]</i> “The delivery terms applicable to this Contract are delivered <i>[indicate place of destination]</i>. Risk and title will pass from the Supplier to the Procuring Entity upon receipt and final acceptance of the Goods at their final destination.”</p> <p>Delivery of the Goods shall be made by the Supplier in accordance with the terms specified in Section VI (Schedule of Requirements).</p> <p>For purposes of this Clause the Procuring Entity’s Representative at the Project Site is <i>[indicate name(s)]</i>.</p> <p><b>Incidental Services –</b></p> <p>The Supplier is required to provide all of the following services, including additional services, if any, specified in Section VI. Schedule of Requirements: <i>Select appropriate requirements and delete the rest.</i></p> <ul style="list-style-type: none"> <li>a. performance or supervision of on-site assembly and/or start-up of the supplied Goods;</li> <li>b. furnishing of tools required for assembly and/or maintenance of the supplied Goods;</li> <li>c. furnishing of a detailed operations and maintenance manual for each appropriate unit of the supplied Goods;</li> <li>d. performance or supervision or maintenance and/or repair of the supplied Goods, for a period of time agreed by the parties, provided that this service shall not relieve the Supplier of any warranty obligations under this Contract; and</li> </ul>
	<ul style="list-style-type: none"> <li>e. training of the Procuring Entity’s personnel, at the Supplier’s plant and/or on-site, in assembly, start-up, operation, maintenance, and/or repair of the supplied Goods.</li> <li>f. <i>[Specify additional incidental service requirements, as needed.]</i></li> </ul> <p>The Contract price for the Goods shall include the prices charged by the Supplier for incidental services and shall not exceed the prevailing rates charged to other parties by the Supplier for similar services.</p>

	<p><b>Spare Parts –</b></p> <p>The Supplier is required to provide all of the following materials, notifications, and information pertaining to spare parts manufactured or distributed by the Supplier:</p> <p><i>Select appropriate requirements and delete the rest.</i></p> <ul style="list-style-type: none"><li>a. such spare parts as the Procuring Entity may elect to purchase from the Supplier, provided that this election shall not relieve the Supplier of any warranty obligations under this Contract; and</li><li>b. in the event of termination of production of the spare parts:<ul style="list-style-type: none"><li>i. advance notification to the Procuring Entity of the pending termination, in sufficient time to permit the Procuring Entity to procure needed requirements; and</li><li>ii. following such termination, furnishing at no cost to the Procuring Entity, the blueprints, drawings, and specifications of the spare parts, if requested.</li></ul></li></ul> <p>The spare parts and other components required are listed in <b>Section VI (Schedule of Requirements)</b> and the cost thereof are included in the contract price.</p> <p>The Supplier shall carry sufficient inventories to assure ex-stock supply of consumable spare parts or components for the Goods for a period of <i>[indicate here the time period specified. If not used indicate a time period of three times the warranty period]</i>.</p> <p>Spare parts or components shall be supplied as promptly as possible, but in any case, within <i>[insert appropriate time period]</i> months of placing the order.</p>
	<p><b>Packaging –</b></p> <p>The Supplier shall provide such packaging of the Goods as is required to prevent their damage or deterioration during transit to their final destination, as indicated in this Contract. The packaging shall be sufficient to withstand, without limitation, rough handling during transit and exposure to extreme temperatures, salt and precipitation during transit, and open storage. Packaging case size and weights shall take into consideration, where appropriate, the remoteness of the Goods’ final destination and the absence of heavy handling facilities at all points in transit.</p> <p>The packaging, marking, and documentation within and outside the packages shall comply strictly with such special requirements as shall be expressly provided for in the Contract, including additional requirements, if any, specified below, and in any subsequent instructions ordered by the Procuring Entity.</p> <p>The outer packaging must be clearly marked on at least four (4) sides as follows:</p> <p>Name of the Procuring Entity Name of the Supplier</p>

	<p>Contract Description</p> <p>Final Destination</p> <p>Gross weight</p> <p>Any special lifting instructions</p> <p>Any special handling instructions</p> <p>Any relevant HAZCHEM classifications</p>
	<p>A packaging list identifying the contents and quantities of the package is to be placed on an accessible point of the outer packaging if practical. If not practical the packaging list is to be placed inside the outer packaging but outside the secondary packaging.</p> <p><b>Transportation –</b></p> <p>Where the Supplier is required under Contract to deliver the Goods CIF, CIP, or DDP, transport of the Goods to the port of destination or such other named place of destination in the Philippines, as shall be specified in this Contract, shall be arranged and paid for by the Supplier, and the cost thereof shall be included in the Contract Price.</p> <p>Where the Supplier is required under this Contract to transport the Goods to a specified place of destination within the Philippines, defined as the Project Site, transport to such place of destination in the Philippines, including insurance and storage, as shall be specified in this Contract, shall be arranged by the Supplier, and related costs shall be included in the contract price.</p>
	<p>Where the Supplier is required under Contract to deliver the Goods CIF, CIP or DDP, Goods are to be transported on carriers of Philippine registry. In the event that no carrier of Philippine registry is available, Goods may be shipped by a carrier which is not of Philippine registry provided that the Supplier obtains and presents to the Procuring Entity certification to this effect from the nearest Philippine consulate to the port of dispatch. In the event that carriers of Philippine registry are available but their schedule delays the Supplier in its performance of this Contract the period from when the Goods were first ready for shipment and the actual date of shipment the period of delay will be considered force majeure.</p> <p>The Procuring Entity accepts no liability for the damage of Goods during transit other than those prescribed by INCOTERMS for DDP deliveries. In the case of Goods supplied from within the Philippines or supplied by domestic Suppliers risk and title will not be deemed to have passed to the Procuring Entity until their receipt and final acceptance at the final destination.</p> <p><b>Intellectual Property Rights –</b></p> <p>The Supplier shall indemnify the Procuring Entity against all third-party claims of infringement of patent, trademark, or industrial design rights arising from use of the Goods or any part thereof.</p>
2.2	<i>[If partial payment is allowed, state]</i> “The terms of payment shall be as follows:”
4	The inspections and tests that will be conducted are: <i>Product Presentation/Demonstration/Site Inspection, if applicable.</i>

# Section VI. Schedule of Requirements

## PROJECT NAME: PROCUREMENT OF INDUSTRIAL ELECTRONICS TRAINER (BASIC COMMUNICATION SYSTEM) AND OTHERS

## PROJECT NO. QCU-23-ELTE-1295B

The delivery schedule expressed as weeks/months stipulates hereafter a delivery date which is the date of delivery to the project site.

Item Number	Description	Unit of Issue	Quantity	Delivered, Weeks/Months
1	<p><b>DIGITAL ELECTRONICS FUNDAMENTALS MODULE</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* at least 13 modules cover a large variety of essential topics for digital logic</li> <li>* Suitable for combination logic, sequential logic and microprocessor circuits design and experiments</li> <li>* Experiments are expandable and flexible with universal breadboard</li> <li>* Capable of processing TTL, CMOS, NMOS, PMOS and ECL circuit</li> <li>* All supply units are equipped with overload protection for safety purpose</li> <li>* All modules equipped with 8-bit DIP switch for fault simulations</li> </ul> <p><b>Specification:</b></p> <ol style="list-style-type: none"> <li>Dual DC Power Supply               <ol style="list-style-type: none"> <li>Voltage Range</li> <li>With output overload protection</li> </ol> </li> <li>Adjustable DC Power Supply               <ol style="list-style-type: none"> <li>Voltage range: +1.5V~+15V</li> <li>Maximum current output: 0.5A</li> <li>With output overload protection</li> </ol> </li> <li>Standard Frequency               <ol style="list-style-type: none"> <li>Frequency: 1MHz, 60Hz, 1Hz</li> <li>Accuracy: ±0.01% (1MHz)</li> <li>Fan out: 10 TTL load</li> </ol> </li> <li>Clock Signal Generator               <ol style="list-style-type: none"> <li>Frequency: 1Hz-1MHz (6 ranges)                   <ol style="list-style-type: none"> <li>1Hz ~ 10Hz</li> <li>10Hz ~ 100Hz</li> <li>100Hz ~ 1KHz</li> <li>1KHz~10KHz</li> <li>10KHz~100KHz</li> <li>100KHz~1MHz</li> </ol> </li> <li>Fan out: 10 TTL load</li> </ol> </li> <li>Data Switch               <ol style="list-style-type: none"> <li>8-bit DIP switchx2, 16-bit TTL level output</li> <li>Toggle switchx4, each with DEBOUNCE circuit</li> <li>Fan out: 10 TTL load</li> </ol> </li> <li>Pulser Switch               <ol style="list-style-type: none"> <li>2 sets of independent control output</li> <li>Each set with Q, /Q output, pulse width &gt; 5ms</li> </ol> </li> </ol>	set	1	<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>

	<p>(3) Each set of switch with DEBOUNCE circuit (4) Fan out: 10 TTL load</p> <p>7. Line Signal Generator (1) Frequency: 50 /60Hz (2) Output voltage: 6Vrms (3) With overload protection</p> <p>8. Thumbwheel Switch (1) 2-digit, BCD code output and common point Input</p> <p>9. Logic Indicator (1) 16 sets of Independent LED Indicates high and low logic state (2) Input Impedance: 100KΩ</p> <p>10. Logic Indicator (1) 4 sets of independent 7-segment LED display (2) With BCD, 7-segment decoder/driver and DP input (3) Input with 8-4-2-1 code</p> <p>11. Logic Probe (1) TTL and CMOS level (2) 5mm LED displays (3) "Lo" and "Hi" LED display low and high logic state respectively</p> <p>12. Speaker (1) One 8Ω, 0.25W speaker with driver circuit</p> <p>13. Breadboard Modules (1) 1,680 tie-point breadboard on top panel can be easily put into and taken off</p> <p><b>Experiment Modules Specification:</b>  1. All modules are equipped with an 8-bit DIP switch for fault simulation. Users learn how to solve various problems by setting the DIP switch to different positions  2. Solutions for all fault test are listed in the experiment manual for user's reference.  3. 2mm plugs and sockets are used throughout the main unit and all modules.  4. Comprehensive experiment manual and instructor's manual  5. Module dimension: at least 255mm x 165mm x 30mm  6. Connection plugs are used on the modules to prevent accidental damages.  7. Individual keeping case for each module</p>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
2	<p><b>AM/FM/ASK/FSK TRANSMITTER AND RECEIVER SYSTEM</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>• AM transmitter and Receiver modules</li> <li>• FM transmitter and Receiver modules</li> <li>• ASK/AM Transmitter and Receiver modules</li> <li>• FSK/FM Transmitter and Receiver modules</li> <li>• Comprehensive and self-contained system carrying out AM/ASK and FM/FSK transmission</li> <li>• The Modules equipped with 8-bit DIP switch for fault-finding experiment</li> </ul>	set	1	

<ul style="list-style-type: none"> <li>• Provided with comprehensive experimental manual.</li> <li>• An Actual mini broadcasting Station</li> </ul> <p><b>Specification:</b></p> <p>1) AM/DSB Transmitter</p> <p>a. With perfect AM Transmitter for producing 1MHz</p> <p>b. Equipped with 8-bit DIP switch for circuits fault simulations</p> <p>2) AM Transistorize Radio</p> <p>a. AM Receiver frequency range: 535KHz - 1605KHz</p> <p>b. Intermediate frequency: 455KHz</p> <p>c. Equipped with 8-bit DIP switch for circuits fault simulations</p> <p>3) FM Transmitter</p> <p>a. With perfect FM Transmitter for producing 10.7MHz Intermediate frequency</p> <p>b. Equipped with 8-bit DIP switch for circuits fault simulations</p> <p>c. Digital probe: 1Hz~89999999Hz</p> <p>4) FM Stereo Radio</p> <p>a. FM Receiver frequency range: 88MHz-108MHz</p> <p>b. Frequency shown by 7 segment LED display</p> <p>c. Equipped with 8-bit DIP switch for circuits fault simulations</p> <p>5) ASK/AM Transmitter</p> <p>a. Amplitude-shift keying (ASK) transmitter</p> <p>1) Carrier frequency: 1.0MHz and 1.6MHz</p> <p>b. Data transmission format:</p> <p>1) Start bit</p> <p>2) 64-bit encoded data: 8-bit data encoded by 8-bit spread spectrum set</p> <p>3) Stop bit</p> <p>c. Data transmission:</p> <p>1) Direct modulation</p> <p>2) Manchester encoding</p> <p>d. Data rate:</p> <p>1) 100/62.5Hz</p> <p>2) 160/100Hz</p> <p>3) 1.6K/1KHz</p> <p>e. AM audio modulation signal:</p> <p>1) Audio input: mono microphone input</p> <p>6) ASK/AM Receiver</p> <p>a. AM receiver frequency range: 535KHz ~ 1605KHz</p> <p>b. Intermediate frequency: 455KHz</p> <p>c. Data receive mode:</p> <p>1) Direct demodulation</p> <p>2) Manchester decoding</p> <p>d. Data rate:</p> <p>1) 100/62.5Hz</p> <p>2) 160/100Hz</p> <p>3) 1.6K/1KHz</p> <p>e. AM audio demodulation signal:</p> <p>1) Audio output: 0.2W 8Ω speak</p> <p>(7) FSK/FM Transmitter</p> <p>a. Frequency-shift keying (FSK) transmitter, which is able to produce 10.7 MHz intermediate frequency</p> <p>b. Data transmission format:</p>				<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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<p>1) Start bit  2) 64-bit encoded data: 8-bit data encoded by 8-bit spread spectrum setting  3) Stop bit  c. Data transmission:  1) Direct modulation  2) Manchester encoding  d. Data rate:  1) 100/62.5Hz  2) 160/100Hz  e. FM audio modulation signal:  1) Audio input: Mono microphone input  f. Digital probe: 1Hz~89999999Hz</p> <p>8) FSK/AM Receiver  a. FM receiver frequency: 10.7MHz  b. Data receive mode:  1) Direct demodulation  2) Manchester decoding  c. Data rate:  1) 100/62.5Hz  2) 160/100Hz  d. FM Audio demodulation signal:  1) Audio output: 0.2W 8Ω speaker</p> <p><b>LIST OF EXPERIMENTS:</b></p> <p><b>AM Experiments</b>  1. Sine wave/Voice Modulator  2. Modulator Measurement  3. DSBSC/Sine wave Pattern  4. Modulator percentage  5. Trapezoid Pattern  6. Voice/Antenna Adjust  7. First IF Amplifier  8. Secson IF Amplifier  9. Frequency Transmitter/Receiver and Modulator/Demodulator  10. Music Transmitter and Receiver</p> <p><b>FM Experiments</b>  1. Voltage Measurements of Varactor Diode  2. Crystal Oscillator Measurements  3. Frequency Multiplier Adjustments (For Second Harmonic)  4. RF Amplifier Tuning Circuit Adjustments (For Second Harmonic)  5. Frequency Multiplier Adjustments (For Third Harmonic)  6. RF Amplifier Tuning Circuit Adjustments (For Third Harmonic)  7. Audio Amplifier Measurements  8. Reference Oscillator Measurements  9. FM IF Amplifier  10. Counter  11. Music Transmitter and Receiver</p> <p><b>ASK/AM Experiments</b>  1. RF Amplifier Adjustment (underlined data are reference values)  2. Digital data coding (CDMA and Manchester encoding)  3. Calibration of Transmitter and Receiver</p>				<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p>4. Transmitter and Receiver Communication Experiment</p> <p>5. Analog transmission of digital signals</p> <p>6. Analog signal transmitted by analog transmission</p> <p>7. Music signal transmission</p> <p><b>FSK/FM Experiments</b></p> <p>1. Voltage Measurement of Varactor Diode</p> <p>2. Crystal Oscillator Frequency Measurement</p> <p>3. Frequency Multiplier Adjustment (Doubler)</p> <p>4. Frequency Multiplier Adjustment (Tripler)</p> <p>5. RF Amplifier Tuned Circuit Adjustment</p> <p>6. Digital Data Transmission (Direct Sequence and Manchester Encoding)</p> <p>7. Audio Amplifier Adjustment</p> <p>8. Calibration of RF Amplifier</p> <p>9. Receiver Frequency Detector Adjustment</p> <p>10. Decoding Sequence Adjustment</p> <p>11. Digital Data Transmission and Receiving (Direct Sequence and Manchester Encoding)</p> <p>12. Digital Data Transmission and Receiving Example</p> <p>13. Analog transmission of digital signals</p> <p>14. Analog signal transmitted by analog transmission</p> <p>15. Music signal transmission</p> <p><b>ACCESSORIES</b></p> <p>1. CI -18001 Power Supply x 2</p> <p>(1) Output: <math>\pm 5V</math>, 0.5A; <math>\pm 12V</math>, 0.5A</p> <p>(2) Input: AC 110/220V</p> <p>2. Connection leads and plugs x 1 set</p> <p>3. Telescope antenna x 3 pieces</p> <p>4. Mini-microphone x 2 pieces</p> <p>5. Experiment manual</p> <p>6. Digital logic probe x 1 set</p> <p>7. Storage Cabinet</p> <p>8. Rack Frame</p> <p>9. Digital Storage Oscilloscope with FFT</p>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
3	<p><b>ANALOG AND DIGITAL COMMUNICATION TRAINER (BASIC COMMUNICATION SYSTEM)</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* It offers the beginner complete courses of basic analog and digital communication.</li> <li>* Familiarization with the operative aspects of the work in telecommunication laboratory</li> <li>* The building block and components symbols of the circuits are printed on the surface of each modules</li> </ul> <p><b>List of Modules</b></p> <p>1. Analog Communication Modules</p> <p>(1) Oscillator/Second Order LPF &amp; HPF</p> <p>(2) AM Modulator/Demodulator</p> <p>(3) DSB-SC and SSB Modulator/Demodulator</p> <p>(4) FM Modulator/Demodulator</p> <p>(5) PLL Frequency Synthesizer</p> <p>(6) TDM and PAM-TDM Multiplexer/Demultiplexer</p> <p>(7) FDM Multiplexer/Demultiplexer</p> <p>(8) Signal Converter/Recovery/Regeneration</p> <p>2. Digital Communication Modules</p> <p>(1) A/D, D/A Converter Applications</p> <p>(2) PWM Modulator/Demodulator</p> <p>(3) FSK Modulator/Demodulator</p>	set	1	

	<p>(4) CVSD Modulator/Demodulator, Manchester Code Encode/Decode  (5) ASK Modulator/Demodulator  (6) PSK/QPSK Modulator  (7) PSK/QPSK Demodulator</p> <p>3. Power Supply and Signal Generator Unit  (1) Main Unit</p> <p><b>List of Experiments</b></p> <p><b>Analog Communication</b></p> <ol style="list-style-type: none"> <li>1. RF oscillator experiment</li> <li>2. Second order LPF and HPF experiment</li> <li>3. AM modulator experiment</li> <li>4. AM demodulator experiment</li> <li>5. DSB-SC and SSB modulator experiment</li> <li>6. DSB-SC and SSB demodulator experiment</li> <li>7. FM modulator experiment</li> <li>8. FM demodulator experiment</li> <li>9. Frequency synthesizer</li> <li>10. TDM multiplexer experiments</li> <li>11. TDM demultiplexer experiments</li> <li>12. FDM multiplexer experiments</li> <li>13. FDM demultiplexer experiments</li> <li>14. Frequency converter experiment</li> <li>15. Carrier frequency recovery experiment</li> <li>16. Manchester clock regeneration experiment</li> </ol> <p><b>Digital Communication</b></p> <ol style="list-style-type: none"> <li>1. Analog to digital experiment</li> <li>2. Digital to analog experiment</li> <li>3. PWM modulator experiment</li> <li>4. PWM demodulator experiment</li> <li>5. FSK modulator experiment</li> <li>6. FSK demodulator experiment</li> <li>7. CVSD modulators and demodulators</li> <li>8. Manchester code encode/decode</li> <li>9. ASK modulator/demodulator</li> <li>10. PSK/QPSK modulator/demodulator</li> </ol> <p><b>Accessories:</b></p> <ol style="list-style-type: none"> <li>1. Connector Leads</li> <li>2. Experiment Manual</li> <li>3. Teacher's Guide</li> <li>4. Storage Cabinet x 2</li> <li>5. Rack Frame</li> <li>6. Digital Storage Oscilloscope</li> <li>7. Hand-held Digital Multimeter</li> </ol>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
4	<p><b>ELECTRIC CIRCUIT LAB FUNDAMENTALS</b></p> <p><b>Features:</b></p> <ol style="list-style-type: none"> <li>1. Ideal for electric circuit experiments and design exercises and integrated trainer with complete curriculum</li> <li>2. Complete with power supplies and test systems for easy and efficient experimentation</li> <li>3. With Universal breadboard (1,680 tie-points) for circuit design and prototyping</li> <li>4. All modules equipped with an 8-bit DIP switch for circuits fault simulations or fault insertion</li> </ol> <p><b>Technical Specification:</b></p> <ul style="list-style-type: none"> <li>• Main Unit</li> </ul>	set	1	

<p>1. DC Power Supply</p> <p>1) Fixed DC power supply</p> <p>a. Voltage range: : <math>\pm 5\text{DCV}</math> to <math>-5\text{DCV}</math> , <math>+12\text{DCV}</math> to <math>-12\text{DCV}</math></p> <p>b. With output overload protection</p> <p>2) Dual DC power supply</p> <p>a. <math>\pm 3\text{V}</math> to <math>\pm 18\text{DCV}</math>, continuously adjustable</p> <p>b. With output overload protection</p> <p>2. AC Power Supply</p> <p>1. <math>9\text{ACV}</math> - <math>0\text{ACV}</math> - <math>9\text{ACV}</math></p> <p>2. With output overload protection</p> <p>3. Function Generator</p> <p>Sine, square and triangle</p> <p>10 Hertz to 100 Kilohertz, 4 settings, continuously adjustable</p> <p><math>\pm 5</math> percent of scale value</p> <p>50 Ohms</p> <p><math>\geq 18\text{V}</math> peak to peak (open loop) <math>\geq 9\text{V}</math> peak to peak (with <math>50\Omega</math> load)</p> <p>4. 3 1/2-Digit Digital Voltmeter /Ammeter</p> <p>2DCV to 200DCV</p> <p><math>\pm 0.3\%</math> of reading + 1 digit</p> <p>200<math>\mu\text{A}</math>, to 2000mA</p> <p><math>\pm 0.5\%</math> of reading + 1 digit</p> <p>5. Analog Meters</p> <p>0 to 100mA up to 1A</p> <p>0 to 15V</p> <p>0 to 100mA up to 1A</p> <p>0 to 20V</p> <p>6. Speaker</p> <p>1) <math>8\Omega</math>, 0.25W speaker with driver amplifier circuit</p> <p>7. Variable Resistors</p> <p>1) <math>1\text{K}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</p> <p>2) <math>10\text{K}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</p> <p>3) <math>100\text{K}\Omega</math>, 0.25 W variable resistor with 3 terminals (A,B,C)</p> <p>4) <math>1\text{M}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</p> <p>8. Solderless Breadboard</p> <p>1) 1,680 tie-points breadboard on top panel can be easily put into and taken off</p> <p><b>The Equipment must have the following Experiment Modules:</b></p> <ul style="list-style-type: none"> <li>• Basic Electricity Experiments and Magnetism Element Introduction Module</li> <li>• Magnetic Field, Ampere's Rule, Fleming's Rule Module</li> <li>• Electromagnetic Induction Module</li> <li>• Electronic Circuit Fundamental Experiments Module</li> <li>• Basic Electronic Circuit Experiments (1 and 2) Module</li> <li>• Special Electronic Components Experiments Module</li> <li>• Oscillator Experiments and Applications Module</li> </ul>				<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p><b>The package equipment must have the following list of Experiment</b></p> <p><b>1. Experiments for Basic Electricity</b></p> <p>a. DC voltage measurement, Using an ohmmeter, Resistor characteristics, DC current measurement</p> <p>b. AC voltage and AC Current measurement</p> <p>c. AC RC, RL, and RLC circuit</p> <p>d. Power in AC circuit</p> <p>e. Transformer characteristics</p> <p>f. Series-resonant and Parallel-resonant circuit</p> <p>g. LC filter</p> <p><b>2. Experiments for Magnetism</b></p> <p>a. Magnetic devices and Magnetic field</p> <p>b. Drawing magnetic curves and Magnetic field strength</p> <p>c. Lenz's and Faraday's laws</p> <p>d. Ampere's rule, Fleming's rule and Self-Induction</p> <p>e. Mutual Induction and Magnetic flux detection</p> <p><b>3. Experiments for Basic Electronic Circuits</b></p> <p>a. Diode characteristics, Rectifier circuit, Filter circuit,</p> <p>b. Zener diode, LED, Transistor characteristics</p> <p>c. Multimeter functions</p> <p>d. FET, SCR, UJT characteristics</p> <p><b>4. Experiments for Simple Electronic Circuits</b></p> <p>a. Simple, Complementary, Push-pull and Multistage cascading amplifier</p> <p>b. Voltage regulator</p> <p>c. Wheatstone bridge</p> <p>d. Dimmer circuit</p> <p>e. Relay characteristics</p> <p>f. Touch-controlled switch</p> <p><b>5. Experiments for Industrial Control Applications</b></p> <p>a. CDS and Thermistor characteristics</p> <p>b. Light-controlled, Temperature-controlled and Sound controlled circuit</p> <p><b>6. Experiments for Oscillator Characteristics and Applications</b></p> <p>a. Blocking oscillator</p> <p>b. Electronic birdcall, LC resonant and LED flasher circuit</p> <p>c. Astable multivibrator</p> <p><b>The Equipment must package with the following standard accessories:</b></p> <ul style="list-style-type: none"> <li>• Experiment manual and instructor's manual</li> <li>• Connection leads and plugs: 1 set</li> <li>• Inductors: 0.1H, 0.5H each 1 piece</li> <li>• Magnet: 1 piece</li> <li>• Key (Allen Wrench): 1 piece</li> <li>• Handheld Digital Multi Meter</li> <li>• Digital Oscilloscope</li> </ul>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
5	<p><b>ELECTRONICS FUNDAMENTALS MODULE</b></p> <p><b>Features:</b></p> <p>1. All modules must be equipped with an 8-bit DIP switch for fault insertion/ simulation</p> <p>2. Individual keeping case for easy carrying and Storage.</p>	set	1	

	<p>3. With universal breadboard for designing own circuits.</p> <p>4. Uses 2mm connecting plug for the socket</p> <p><b>Technical Specification:</b></p> <ul style="list-style-type: none"> <li>• Main Unit <ul style="list-style-type: none"> <li>1. DC Power Supply <ul style="list-style-type: none"> <li>1) Fixed DC power supply <ul style="list-style-type: none"> <li>a. Voltage range: +5DCV to - 5DCV, +12DCV to - 12DCV</li> <li>b. With output overload protection</li> </ul> </li> <li>2) Dual DC power supply <ul style="list-style-type: none"> <li>a. <math>\pm 3V</math> to <math>\pm 18</math> DCV, continuously adjustable</li> <li>b. With output overload protection</li> </ul> </li> </ul> </li> <li>2. AC Power Supply <ul style="list-style-type: none"> <li>1. 9ACV - 0ACV - 9ACV</li> <li>2. With output overload protection</li> </ul> </li> <li>3. Function Generator <ul style="list-style-type: none"> <li>Sine, square and triangle</li> <li>10 Hertz to 100 Kilohertz, 4 settings, continuously adjustable</li> <li><math>\pm 5</math> percent of scale value</li> <li>50 Ohms</li> <li><math>\geq 18V</math> peak to peak (open loop) <math>\geq 9V</math> peak to peak (with <math>50\Omega</math> load)</li> </ul> </li> <li>4. 3 1/2-Digit Digital Voltmeter/Ammeter <ul style="list-style-type: none"> <li>2DCV to 200DCV</li> <li><math>\pm 0.3\%</math> of reading + 1 digit</li> <li>200<math>\mu A</math>, to 2000mA</li> <li><math>\pm 0.5\%</math> of reading + 1 digit</li> </ul> </li> <li>5. Analog Meters <ul style="list-style-type: none"> <li>0 to 100mA up to 1A</li> <li>0 to 15V</li> <li>0 to 100mA up to 1A</li> <li>0 to 20V</li> </ul> </li> <li>6. Speaker <ul style="list-style-type: none"> <li>1) <math>8\Omega</math>, 0.25W speaker with driver amplifier circuit</li> </ul> </li> <li>7. Variable Resistors <ul style="list-style-type: none"> <li>1) <math>1K\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>2) <math>10K\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>3) <math>100K\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>4) <math>1MQ</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> </ul> </li> <li>8. Solderless Breadboard <ul style="list-style-type: none"> <li>1) 1,680 tie-points breadboard on top panel can be easily put into and taken off</li> </ul> </li> </ul> <p><b>The equipment modules shall have the following specification:</b></p> <ul style="list-style-type: none"> <li>1. Each module is secured in a solid-body plastic housing and each module shall equipped with DIP switch (8-bit) for fault insertion/simulations. Students</li> </ul> </li></ul>			<p style="text-align: center;"><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p>can practice trouble shooting by setting the DIP switch to different positions</p> <p>2. Detailed solutions for the fault simulation are included in the instructor's manual</p> <p><b>The equipment shall be able to perform the following experiments:</b></p> <ol style="list-style-type: none"> <li>1. Characteristics of Diodes</li> <li>2. Clipping and Clamping Circuits with Diodes</li> <li>3. Rectifier Circuits</li> <li>4. Differential and Integrator Circuits</li> <li>5. Transistors</li> <li>6. Transistor Amplification Circuits</li> <li>7. Field Effect Transistors (FET)</li> <li>8. FET Amplification Circuits</li> <li>9. Multi - Stage Amplification Circuits</li> <li>10. Transistor Negative Feedback Circuits</li> <li>11. Transistor Positive Feedback Circuits</li> <li>12. Regulated Voltage /Constant Current Circuits</li> <li>13. Modulation and Demodulation</li> <li>14. Op Amp</li> <li>15. Basic Characteristics of OP Amplifier</li> <li>16. Basic Characteristics of OP Amplifier (1 and 2) - Negative Feedback</li> <li>17. Basic Characteristics of OP Amplifier - Positive Feedback</li> </ol> <p><b>The set shall come with the following accessories:</b></p> <ol style="list-style-type: none"> <li>1. Experiment manual and instructor's manual</li> <li>2. Connection leads and plugs (2mm): 1 set</li> <li>3. Allen wrench Key: 1 piece</li> <li>4. Handheld Digital Multi Meter</li> <li>5. Digital Oscilloscope</li> </ol> <p><b>The software shall have the following capabilities:</b></p> <ol style="list-style-type: none"> <li>1. Schematic Editor</li> <li>2. Schematic Symbol Editor</li> <li>3. Footprint Editor</li> <li>4. Netlist Editor</li> <li>5. Live 3D Breadboard Tool</li> <li>6. Advanced PCB Designer <ol style="list-style-type: none"> <li>1) Automatic and Manual Design Tools</li> <li>2) Creating flex PCB's</li> <li>3) Animated 3D view</li> </ol> </li> <li>7. Electrical Rules Check (ERC)</li> <li>8. Interpreter</li> <li>9. Library Manager</li> <li>10. Parameter Extractor</li> <li>11. Text and Equation Editor</li> <li>12. DC analysis</li> <li>13. Transient Analysis</li> <li>14. Fourier analysis</li> <li>15. Digital Simulation</li> <li>16. HDL Simulation (VHDL, Verilog, Verilog-A, Verilog-AMS)</li> <li>17. Microcontrollers (MCU) Simulation <ol style="list-style-type: none"> <li>1) MCU Simulation and Debugging (PIC, AVR, 8051,8085, HCS, ARM)</li> <li>2) Linux and Android Simulation (ARM)</li> <li>3) Mixed Spice Simulation (Berkeley and XSpice)</li> </ol> </li> <li>18. Flowchart Editor and Debugger</li> </ol>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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19. AC analysis 20. Network analysis 21. Noise analysis 22. Symbolic analysis 23. Monte-Carlo and Worst-case analysis 24. Design Tool 25. Optimization 26. Post-processor 27. Presentation 28. Interactive mode 29. Virtual Instruments 1) Digital Multimeter 2) Function Generator 3) Storage Oscilloscope 4) Signal and Network Analyzer 5) Digital Signal Generator 6) Logic Analyzer 30. Real-time Test and Measurements 31. Mechatronics Extension 1) Sensing and Controlling 2) Robotics 3) Automotive Electronics 32. Faster MCU simulation, real time simulation of delays 33. Verilog-AMS: analogue events in digital context 34. New format at Fourier analysis: $D * \cos(kwt + fi)$ 35. The software accepts both M and MEG as mega (1e6), but mega is always displayed as MEG (Spice) 36. Macro wizard: Configurable pin order & name at auto generated shapes 37. Zoom or Scroll with mouse wheel, as set at View Options 38. New features in the HDL editor 39. Added reference voltage in the Fourier spectrum dialog 40. Added MicroBasic 1st file handling support in the hex/1st debugger 41. Multilanguage capability, instant language change for licensed languages 42. Open project files with preview of schematics and mechatronics 43. EDIF import 44. Global Spice variables 45. KLU - a faster linear solver and better convergence on large-size or special circuits 46. Enhanced and accelerated VHDL and Verilog simulation 10x times faster than in v9 47. Xilinx simprim simulation in digital and in mixed mode 48. Verilog A 49. Verilog AMS 50. MCU C compiling on 8051, AVR, PIC16, PIC18, PIC32, ARM 51. Spice compatible AD-DA interface 52. Oscilloscope works together with the interactive mode 53. Frequency and waveform parameters are shown with cursors 54. Hotkeys to AC and Transient Analysis 55. Cursor Max and Min functions added to the Process menu				<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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6	<p><b>FIBER OPTIC COMMUNICATION TRAINER</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* With four different data transmission ways (self-module transmission, module-to-module transmission, PC-to-module transmission, and module-to-PC to-module transmission)</li> <li>* The experiment will show you how easy it is to make productive use of fiber optic materials.</li> <li>* The equipment that you assemble will transmit voice from one point to another, using light traveling through an optical fiber.</li> </ul> <p><b>Specifications:</b></p> <p>Main Unit</p> <ol style="list-style-type: none"> <li>1. Power: AC-DC Adapter <ol style="list-style-type: none"> <li>a. AC input: 100V ~ 240V</li> <li>b. DC output: 15V, 500mA</li> </ol> </li> <li>2. Microphone Circuit <ol style="list-style-type: none"> <li>a. Frequency range: 20Hz ~ 12KHz</li> <li>b. With gain 20 amplified circuit</li> </ol> </li> <li>3. Push-button Switch <ol style="list-style-type: none"> <li>a. N.O. Type</li> <li>b. With LED indication</li> </ol> </li> <li>4. Function generator <ol style="list-style-type: none"> <li>a. Output sine wave with adjustable output amplitude</li> <li>b. Output square wave, with CMOS level</li> <li>c. Frequency range: 6Hz ~ 2KHz</li> </ol> </li> <li>5. Output Speaker <ol style="list-style-type: none"> <li>a. 8Ω, 1/4W</li> </ol> </li> <li>6. Transmitter Module <ol style="list-style-type: none"> <li>a. Optical fiber light: Red LED, <math>\lambda = 660\text{nm}</math></li> <li>b. Max. drive current: 50mA</li> <li>c. Effective coupling micro-lens spotlight</li> <li>d. Emitter follower</li> </ol> </li> <li>7. Receiver Module <ol style="list-style-type: none"> <li>a. Optical receiving diode <ol style="list-style-type: none"> <li>i. <math>\lambda</math> peak: 880nm</li> <li>ii. Connectable plastic optical fiber with 1,000m core</li> <li>iii. Effective coupling micro lens spotlight</li> <li>iv. Max. consumption power: 100mW</li> </ol> </li> <li>b. With amplified, gain, restoring sharpness circuit</li> </ol> </li> <li>8. Data transmission elements <ol style="list-style-type: none"> <li>a. Chip set: AVR8515, 8bits, 8MHz crystal</li> <li>b. LCD: back light 20 x 2 letter chip</li> <li>c. Keyboard: 4 x 4 16Key</li> <li>d. Character mode: single letter or string letter available</li> <li>e. Send mode: OFF (self-module transmission), transceiver (module-to-module), PC module PC</li> <li>f. With reset function</li> <li>g. Communication interface: RS 232C, 9600 baud rate</li> <li>h. Software environment: Windows base</li> </ol> </li> </ol>	set	1	<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p><b>Experiment Modules</b></p> <ol style="list-style-type: none"> <li>1. 2mm connection leads are used throughout the system</li> <li>2. The building blocks and components symbols of the circuits are printed on the surface of each module</li> <li>3. Modules are secured in plastic housings</li> <li>4. Comprehensive experimental manual</li> <li>5. Use bridge plugs on circuit loop to reduce the possibility of errors</li> </ol> <p><b>List of Experiments</b></p> <ol style="list-style-type: none"> <li>1. Characteristic of fiber optics experiment</li> <li>2. Applications of fiber optics experiment</li> <li>3. Light sources of fiber optics</li> <li>4. Light and fiber optics interaction experiment</li> <li>5. Fiber optic transmitters experiment</li> <li>6. Receivers for fiber optic system experiment</li> <li>7. Fiber optic expand and network experiment</li> <li>8. Fiber optic connectors and lose-polishing experiment</li> <li>9. Fiber optical data-transmission-self-transceiver experiment</li> <li>10. Fiber optical data-transmission-double-transceiver experiment</li> <li>11. Fiber optical data-transmission - PC module experiment</li> <li>12. Fiber optical data-transmission - module PC experiment</li> </ol> <p><b>Accessories</b></p> <ol style="list-style-type: none"> <li>1. 2mm test lead: 1 set</li> <li>2. Plastic fiber optics: 1 set</li> <li>3. Experiment manual</li> <li>4. RS-232 to USB adapter</li> <li>5. Connection plug pitch=10mm</li> <li>6. Headphone and microphone</li> </ol>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
7	<p><b>INDUSTRIAL ELECTRONICS TRAINER</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* Comprehensive study including the theoretical study and practical exercises</li> <li>* Use of industrial-type components, devices and circuits</li> <li>* The Industrial Electronic Trainer is a self-contained training equipment allowing students to learn more than 70 experiments through a power supply unit and 16 replaceable modules.</li> </ul> <p><b>Specification:</b></p> <ol style="list-style-type: none"> <li>1. Power Supply Unit               <ol style="list-style-type: none"> <li>(1) ACV output voltage: 18V-0V-18V, 0.5A</li> <li>(2) ACV output voltage: 12V-0V-12V, 0.5A</li> <li>(3) DC output voltage: +12V, 0.5A</li> <li>(4) DCV output voltage: +5V, 0.5A</li> </ol> </li> <li>2. Meter/Motor Unit               <ol style="list-style-type: none"> <li>(1) Dual-scale ACV: 150V-300V, class 2.5</li> <li>(2) Dual-scale ACA: 0-100mA-1A, class 2.5</li> <li>(3) Dual-scale DCV: 0-10V-20V, class 2.5</li> <li>(4) Dual-scale DCA: 0-100mA-1A, class 2.5</li> <li>(5) AC 110V/220V motor</li> </ol> </li> </ol>	set	1	

	<p><b>List of Experiments</b></p> <p><b>1. Power Supply Unit Experiments</b>  (1) AC voltage measurement  (2) DC voltage measurement</p> <p><b>2. UJT Experiments</b>  UJT Characteristic and Equivalent Circuit  (1) UJT introduction  (2) UJT characteristic  (3) UJT equivalent circuit  (4) CDS trigger, RTH trigger</p> <p>UT Oscillator Circuit and Timer Switch  (1) UJT relaxation oscillator  (2) UJT timer switch</p> <p><b>3. PUT Experiments</b>  PUT Characteristic and Equivalent Circuit  (1) PUT introduction  (2) PUT characteristic  (3) PUT equivalent circuit  (4) CDS trigger  (5) RTH trigger  PUT Oscillator Circuit and Timer Switch  (1) PUT circuit oscillator  (2) PUT timer switch</p> <p><b>4. PUT and SCR Experiments</b>  PUT Staircase Generator and Voltage Control Ramp Circuit  (1) PUT staircase generator circuit  (2) PUT voltage control ramp circuit  SCR Characteristic and RC Shift Control Circuit  (1) SCR principle  (2) SCR characteristic curve  (3) SCR construction  (4) SCR trigger mode  (5) SCR RC phase control circuit</p> <p><b>5. SCS Experiments</b>  SCS Characteristic Experiment  (1) SCS construction and operation mode  (2) Use VOM meter measuring SCS  (3) SCS Schmitt circuit  (4) SCS simulate PUT circuit  SCS Trigger Circuit Experiment  (1) CDS trigger  (2) RTH trigger</p> <p><b>6. UJT and PUT Trigger SCR Experiments</b>  UT Trigger SCR Phase Control Circuit  (1) Phase control basic circuit  (2) Phase control analysis  (3) AC phase control circuit analysis  (4) UJT trigger SCR phase control circuit  PUT Trigger SCR Phase Control Circuit</p> <p><b>7. SCR Control DC Motor and DIAC, TRIAC</b>  SCR Characteristic Experiments  SCR Control DC Motor Forward/Reverse Experiment  (1) SCR cut-off principle  (2) SCR control DC motor forward/reverse control experiment</p>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p>DIAC, TRIAC Characteristic Experiment</p> <p>(1) DIAC construction and characteristic</p> <p>(2) DIAC operation mode and measurement</p> <p>(3) TRIAC construction and characteristic</p> <p>(4) TRIAC trigger mode</p> <p>(5) TRIAC static measurement</p> <p><b>8. Automatic Control Lamp, TRIAC Control Speed Experiments</b></p> <p>Automatic Control Lamp Experiment</p> <p>(1) TRIAC shift control</p> <p>(2) TRIAC automatic control lamp experiment</p> <p>TRIAC Control Motor Speed Experiment</p> <p>(1) Different motor Introduction</p> <p>(2) TRIAC control motor speed experiment</p> <p><b>9. Temperature Ratio, Photo-Couple and Touch Control Experiments</b></p> <p>Bridge Temperature Ratio Control Experiment</p> <p>(1) Electronic component of thermal resistor</p> <p>(2) SCR bridge temperature ratio control experiment</p> <p>Photo-Couple and Touch Control Experiment</p> <p>(1) Photo-couple control circuit</p> <p>(2) FET construction and characteristic</p> <p>(3) Touch alarm circuit</p> <p><b>10. Over/Under Voltage Breaker and Flasher Control Experiments</b></p> <p>Over/Under Voltage Breaker Experiment</p> <p>(1) OPA characteristic with reverse &amp; non-reverse circuit</p> <p>(2) Voltage comparison circuit</p> <p>Flasher Control Experiment</p> <p>(1) Application of TRIAC power control</p> <p>(2) AC circuit control</p> <p>(3) Multivibrator</p> <p><b>11. TRIAC Liquid Level and IC Timer Switch Experiments</b></p> <p>TRIAC Liquid Level Control Experiment</p> <p>(1) Digital circuit introduction</p> <p>(2) TRIAC liquid level control experiment</p> <p>IC Timer Switch Experiment</p> <p>(1) NE 555 IC circuit introduction</p> <p>(2) IC timer switch experiment</p> <p><b>12. Digital Signal Driver and Zero-Voltage Switch Experiments</b></p> <p>Digital Signal Driver Control Experiment</p> <p>Zero-Voltage Switch Experiments (I)</p> <p>Ideal half-wave zero-voltage switch experiments</p> <p><b>13. Zero-Voltage Switch Experiments</b></p> <p>Zero-Voltage Switch Experiments (II)</p> <p>(1) TRIAC zero-voltage switch experiments</p> <p>(2) IC mode zero-voltage switch experiments</p> <p><b>14. SCR Converter Experiments</b></p> <p>(1) Parallel converter introduction</p> <p>(2) Series converter introduction</p> <p>(3) Converter trigger source</p> <p>(4) Converter voltage adjustment</p> <p>(5) Converter output-waveform improvement</p>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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	<p><b>15. SCR Rectifier Circuit Experiments</b>  (1) Single-phase half-wave rectifier  (2) Single-phase full-wave rectifier  (3) Single-phase bridge rectifier  (4) Three-phase half-wave rectifier  (5) Three-phase full-wave rectifier</p> <p><b>16. JFET/MOSFET Characteristic and MOSFET Speed Control Experiments</b>  (1) JFET characteristic experiment  (2) MOSFET characteristic experiment  (3) MOSFET speed control experiment</p> <p><b>17. IGBT Characteristic and IGBT Speed Control Experiments</b>  (1) IGBT characteristic experiment  (2) IGBT speed control experiment</p> <p><b>18. Accessories (Including Storage Cabinet 2 pieces)</b></p> <p><b>19. Rack Frame</b></p> <p><b>20. Power Supply (0+-30V, 3A)</b></p> <p><b>21. Transformer (For local 3 phase power not 220V)</b></p> <p><b>22. Digital Storage Oscilloscope</b></p> <p><b>23. Hand-held digital multimeter</b></p>			
	<p><b>Terms and Conditions:</b></p> <p>1. Attach proof of ISO certification to ensure that the quality of products and services complies with industry and international standards.  2. Bidder must submit brochure catalog indicating the brand name and model of bid items as additional requirements.  3. Supplier must conduct after sales training for the end-user after the completion of delivery for 18 Engineering faculty members and Laboratory personnel for 5 days.  4. Supplier must provide continuous technical support to equipment parts including services and repair as long as the equipment is functional.  5. Equipment must be supplied with training manuals in English.  6. Please attach Trainor's certification from the manufacturer to assure the quality of training that will be provided.</p> <p><b>After Sales Service:</b></p> <ul style="list-style-type: none"> <li>• Free trainer's training</li> <li>• Free Semi-Annual Re-training with certificate, on request.</li> <li>• Free Consultation/Seminar, on request</li> <li>• Free Annual product inspection, on request</li> <li>• Free Product demonstration, on Request</li> </ul>			<p><b>Within One Hundred Twenty (120) Calendar Days Upon Issuance of Notice to Proceed</b></p>
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I hereby certify to comply and deliver all the above requirements.

Name: \_\_\_\_\_

Legal Capacity: \_\_\_\_\_

Signature: \_\_\_\_\_

Duly authorized to sign the Bid for and behalf of: \_\_\_\_\_

# Section VII. Technical Specifications

## Notes for Preparing the Technical Specifications

A set of precise and clear specifications is a prerequisite for Bidders to respond realistically and competitively to the requirements of the Procuring Entity without qualifying their Bids. In the context of Competitive Bidding, the specifications (*e.g.* production/delivery schedule, manpower requirements, and after-sales service/parts, descriptions of the lots or items) must be prepared to permit the widest possible competition and, at the same time, present a clear statement of the required standards of workmanship, materials, and performance of the goods and services to be procured. Only if this is done will the objectives of transparency, equity, efficiency, fairness, and economy in procurement be realized, responsiveness of bids be ensured, and the subsequent task of bid evaluation and post-qualification facilitated. The specifications should require that all items, materials and accessories to be included or incorporated in the goods be new, unused, and of the most recent or current models, and that they include or incorporate all recent improvements in design and materials unless otherwise provided in the Contract.

Samples of specifications from previous similar procurements are useful in this respect. The use of metric units is encouraged. Depending on the complexity of the goods and the repetitiveness of the type of procurement, it may be advantageous to standardize the General Technical Specifications and incorporate them in a separate subsection. The General Technical Specifications should cover all classes of workmanship, materials, and equipment commonly involved in manufacturing similar goods. Deletions or addenda should then adapt the General Technical Specifications to the particular procurement.

Care must be taken in drafting specifications to ensure that they are not restrictive. In the specification of standards for equipment, materials, and workmanship, recognized Philippine and international standards should be used as much as possible. Where other particular standards are used, whether national standards or other standards, the specifications should state that equipment, materials, and workmanship that meet other authoritative standards, and which ensure at least a substantially equal quality than the standards mentioned, will also be acceptable. The following clause may be inserted in the Special Conditions of Contract or the Technical Specifications.

### Sample Clause: Equivalency of Standards and Codes

Wherever reference is made in the Technical Specifications to specific standards and codes to be met by the goods and materials to be furnished or tested, the provisions of the latest edition or revision of the relevant standards and codes shall apply, unless otherwise expressly stated in the Contract. Where such standards and codes are national or relate to a particular country or region, other authoritative standards that ensure substantial equivalence to the standards and codes specified will be acceptable.

Reference to brand name and catalogue number should be avoided as far as possible; where unavoidable they should always be followed by the words “*or at least equivalent.*” References to brand names cannot be used when the funding source is the GOP.

Where appropriate, drawings, including site plans as required, may be furnished by the Procuring Entity with the Bidding Documents. Similarly, the Supplier may be requested to provide drawings or samples either with its Bid or for prior review by the Procuring Entity during contract execution.

Bidders are also required, as part of the technical specifications, to complete their statement of compliance demonstrating how the items comply with the specification.

# Technical Specifications

## PROJECT NAME: PROCUREMENT OF INDUSTRIAL ELECTRONICS TRAINER (BASIC COMMUNICATION SYSTEM) AND OTHERS

## PROJECT NO. QCU-23-ELTE-1295B

Item	Specification	Statement of Compliance
		[Bidders must state here either “Comply” or “Not Comply” against each of the individual parameters of each Specification stating the corresponding performance parameter of the equipment offered. Statements of “Comply” or “Not Comply” must be supported by evidence in a Bidders Bid and cross-referenced to that evidence. Evidence shall be in the form of manufacturer’s un-amended sales literature, unconditional statements of specification and compliance issued by the manufacturer, samples, independent test data etc., as appropriate. A statement that is not supported by evidence or is subsequently found to be contradicted by the evidence presented will render the Bid under evaluation liable for rejection. A statement either in the Bidder's statement of compliance or the supporting evidence that is found to be false either during Bid evaluation, post-qualification or the execution of the Contract may be regarded as fraudulent and render the Bidder or supplier liable for prosecution subject to the applicable laws and issuances.]
A.	With minimum technical specifications:	
1	<b>DIGITAL ELECTRONICS FUNDAMENTALS MODULE</b>  <b>Features:</b> <ul style="list-style-type: none"> <li>* at least 13 modules cover a large variety of essential topics for digital logic</li> <li>* Suitable for combination logic, sequential logic and microprocessor circuits design and experiments</li> <li>* Experiments are expandable and flexible with universal breadboard</li> <li>* Capable of processing TTL, CMOS, NMOS, PMOS and ECL circuit</li> <li>* All supply units are equipped with overload protection for safety purpose</li> <li>* All modules equipped with 8-bit DIP switch for fault simulations</li> </ul> <b>Specification:</b> <ol style="list-style-type: none"> <li>Dual DC Power Supply               <ol style="list-style-type: none"> <li>Voltage Range</li> <li>With output overload protection</li> </ol> </li> <li>Adjustable DC Power Supply               <ol style="list-style-type: none"> <li>Voltage range: +1.5V~+15V</li> <li>Maximum current output: 0.5A</li> <li>With output overload protection</li> </ol> </li> <li>Standard Frequency               <ol style="list-style-type: none"> <li>Frequency: 1MHz, 60Hz, 1Hz</li> <li>Accuracy: ±0.01% (1MHz)</li> <li>Fan out: 10 TTL load</li> </ol> </li> <li>Clock Signal Generator               <ol style="list-style-type: none"> <li>Frequency: 1Hz-1MHz (6 ranges)                   <ol style="list-style-type: none"> <li>1Hz ~ 10Hz</li> <li>10Hz ~ 100Hz</li> <li>100Hz ~ 1KHz</li> <li>1KHz~10KHz</li> <li>10KHz~100KHz</li> <li>100KHz~1MHz</li> </ol> </li> </ol> </li> </ol>	

	<p>(2) Fan out: 10 TTL load</p> <p>5. Data Switch</p> <p>(1) 8-bit DIP switchx2, 16-bit TTL level output</p> <p>(2) Toggle switchx4, each with DEBOUNCE circuit</p> <p>(3) Fan out: 10 TTL load</p> <p>6. Pulser Switch</p> <p>(1) 2 sets of independent control output</p> <p>(2) Each set with Q, /Q output, pulse width &gt; 5ms</p> <p>(3) Each set of switch with DEBOUNCE circuit</p> <p>(4) Fan out: 10 TTL load</p> <p>7. Line Signal Generator</p> <p>(1) Frequency: 50 /60Hz</p> <p>(2) Output voltage: 6Vrms</p> <p>(3) With overload protection</p> <p>8. Thumbwheel Switch</p> <p>(1) 2-digit, BCD code output and common point Input</p> <p>9. Logic Indicator</p> <p>(1) 16 sets of Independent LED Indicates high and low logic state</p> <p>(2) Input Impedance: 100K<math>\Omega</math></p> <p>10. Logic Indicator</p> <p>(1) 4 sets of independent 7-segment LED display</p> <p>(2) With BCD, 7-segment decoder/driver and DP input</p> <p>(3) Input with 8-4-2-1 code</p> <p>11. Logic Probe</p> <p>(1) TTL and CMOS level</p> <p>(2) 5mm LED displays</p> <p>(3) "Lo" and "Hi" LED display low and high logic state respectively</p> <p>12. Speaker</p> <p>(1) One 8<math>\Omega</math>, 0.25W speaker with driver circuit</p> <p>13. Breadboard Modules</p> <p>(1) 1,680 tie-point breadboard on top panel can be easily put into and taken off</p> <p><b>Experiment Modules Specification:</b></p> <p>1. All modules are equipped with an 8-bit DIP switch for fault simulation. Users learn how to solve various problems by setting the DIP switch to different positions</p> <p>2. Solutions for all fault test are listed in the experiment manual for user's reference.</p> <p>3. 2mm plugs and sockets are used throughout the main unit and all modules.</p> <p>4. Comprehensive experiment manual and instructor's manual</p> <p>5. Module dimension: at least 255mm x 165mm x 30mm</p> <p>6. Connection plugs are used on the modules to prevent accidental damages.</p> <p>7. Individual keeping case for each module</p>	
2	<b>AM/FM/ASK/FSK TRANSMITTER AND RECEIVER SYSTEM</b>	



	<p><b>Features:</b></p> <ul style="list-style-type: none"> <li>• AM transmitter and Receiver modules</li> <li>• FM transmitter and Receiver modules</li> <li>• ASK/AM Transmitter and Receiver modules</li> <li>• FSK/FM Transmitter and Receiver modules</li> <li>• Comprehensive and self-contained system carrying out AM/ASK and FM/FSK transmission</li> <li>• The Modules equipped with 8-bit DIP switch for fault-finding experiment</li> <li>• Provided with comprehensive experimental manual.</li> <li>• An Actual mini broadcasting Station</li> </ul> <p><b>Specification:</b></p> <p>1) AM/DSB Transmitter</p> <ol style="list-style-type: none"> <li>a. With perfect AM Transmitter for producing 1MHz</li> <li>b. Equipped with 8-bit DIP switch for circuits fault simulations</li> </ol> <p>2) AM Transistorize Radio</p> <ol style="list-style-type: none"> <li>a. AM Receiver frequency range: 535KHz - 1605KHz</li> <li>b. Intermediate frequency: 455KHz</li> <li>c. Equipped with 8-bit DIP switch for circuits fault simulations</li> </ol> <p>3) FM Transmitter</p> <ol style="list-style-type: none"> <li>a. With perfect FM Transmitter for producing 10.7MHz Intermediate frequency</li> <li>b. Equipped with 8-bit DIP switch for circuits fault simulations</li> <li>c. Digital probe: 1Hz~89999999Hz</li> </ol> <p>4) FM Stereo Radio</p> <ol style="list-style-type: none"> <li>a. FM Receiver frequency range: 88MHz-108MHz</li> <li>b. Frequency shown by 7 segment LED display</li> <li>c. Equipped with 8-bit DIP switch for circuits fault simulations</li> </ol> <p>5) ASK/AM Transmitter</p> <ol style="list-style-type: none"> <li>a. Amplitude-shift keying (ASK) transmitter</li> </ol> <ol style="list-style-type: none"> <li>1) Carrier frequency: 1.0MHz and 1.6MHz</li> <li>b. Data transmission format: <ol style="list-style-type: none"> <li>1) Start bit</li> <li>2) 64-bit encoded data: 8-bit data encoded by 8-bit spread spectrum set</li> <li>3) Stop bit</li> </ol> </li> <li>c. Data transmission: <ol style="list-style-type: none"> <li>1) Direct modulation</li> <li>2) Manchester encoding</li> </ol> </li> <li>d. Data rate: <ol style="list-style-type: none"> <li>1) 100/62.5Hz</li> <li>2) 160/100Hz</li> <li>3) 1.6K/1KHz</li> </ol> </li> <li>e. AM audio modulation signal: <ol style="list-style-type: none"> <li>1) Audio input: mono microphone input</li> </ol> </li> </ol> <p>6) ASK/AM Receiver</p> <ol style="list-style-type: none"> <li>a. AM receiver frequency range: 535KHz ~ 1605KHz</li> <li>b. Intermediate frequency: 455Khz</li> <li>c. Data receive mode: <ol style="list-style-type: none"> <li>1) Direct demodulation</li> <li>2) Manchester decoding</li> </ol> </li> <li>d. Data rate: <ol style="list-style-type: none"> <li>1) 100/62.5Hz</li> </ol> </li> </ol>	
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<p>2) 160/100Hz  3) 1.6K/1KHz  e. AM audio demodulation signal:  1) Audio output: 0.2W 8Ω speak</p> <p>(7) FSK/FM Transmitter  a. Frequency-shift keying (FSK) transmitter, which is able to produce 10.7 MHz intermediate frequency  b. Data transmission format:  1) Start bit  2) 64-bit encoded data: 8-bit data encoded by 8-bit spread spectrum setting  3) Stop bit  c. Data transmission:  1) Direct modulation  2) Manchester encoding  d. Data rate:  1) 100/62.5Hz  2) 160/100Hz  e. FM audio modulation signal:  1) Audio input: Mono microphone input  f. Digital probe: 1Hz~89999999Hz</p> <p>8) FSK/AM Receiver  a. FM receiver frequency: 10.7MHz  b. Data receive mode:  1) Direct demodulation  2) Manchester decoding  c. Data rate:  1) 100/62.5Hz  2) 160/100Hz  d. FM Audio demodulation signal:  1) Audio output: 0.2W 8Ω speaker</p> <p><b>LIST OF EXPERIMENTS:</b></p> <p><b>AM Experiments</b>  1. Sine wave/Voice Modulator  2. Modulator Measurement  3. DSBSC/Sine wave Pattern  4. Modulator percentage  5. Trapezoid Pattern  6. Voice/Antenna Adjust  7. First IF Amplifier  8. Secson IF Amplifier  9. Frequency Transmitter/Receiver and Modulator/Demodulator  10. Music Transmitter and Receiver</p> <p><b>FM Experiments</b>  1. Voltage Measurements of Varactor Diode  2. Crystal Oscillator Measurements  3. Frequency Multiplier Adjustments (For Second Harmonic)  4. RF Amplifier Tuning Circuit Adjustments (For Second Harmonic)  5. Frequency Multiplier Adjustments (For Third Harmonic)  6. RF Amplifier Tuning Circuit Adjustments (For Third Harmonic)  7. Audio Amplifier Measurements  8. Reference Oscillator Measurements  9. FM IF Amplifier  10. Counter</p>	
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	<p>11. Music Transmitter and Receiver</p> <p><b>ASK/AM Experiments</b></p> <ol style="list-style-type: none"> <li>1. RF Amplifier Adjustment (underlined data are reference values)</li> <li>2. Digital data coding (CDMA and Manchester encoding)</li> <li>3. Calibration of Transmitter and Receiver</li> <li>4. Transmitter and Receiver Communication Experiment</li> <li>5. Analog transmission of digital signals</li> <li>6. Analog signal transmitted by analog transmission</li> <li>7. Music signal transmission</li> </ol> <p><b>FSK/FM Experiments</b></p> <ol style="list-style-type: none"> <li>1. Voltage Measurement of Varactor Diode</li> <li>2. Crystal Oscillator Frequency Measurement</li> <li>3. Frequency Multiplier Adjustment (Doubler)</li> <li>4. Frequency Multiplier Adjustment (Tripler)</li> <li>5. RF Amplifier Tuned Circuit Adjustment</li> <li>6. Digital Data Transmission (Direct Sequence and Manchester Encoding)</li> <li>7. Audio Amplifier Adjustment</li> <li>8. Calibration of RF Amplifier</li> <li>9. Receiver Frequency Detector Adjustment</li> <li>10. Decoding Sequence Adjustment</li> <li>11. Digital Data Transmission and Receiving (Direct Sequence and Manchester Encoding)</li> <li>12. Digital Data Transmission and Receiving Example</li> <li>13. Analog transmission of digital signals</li> <li>14. Analog signal transmitted by analog transmission</li> <li>15. Music signal transmission</li> </ol> <p><b>ACCESSORIES</b></p> <ol style="list-style-type: none"> <li>1. CI -18001 Power Supply x 2 (1) Output: <math>\pm 5V</math>, 0.5A; <math>\pm 12V</math>, 0.5A (2) Input: AC 110/220V</li> <li>2. Connection leads and plugs x 1 set</li> <li>3. Telescope antenna x 3 pieces</li> <li>4. Mini-microphone x 2 pieces</li> <li>5. Experiment manual</li> <li>6. Digital logic probe x 1 set</li> <li>7. Storage Cabinet</li> <li>8. Rack Frame</li> <li>9. Digital Storage Oscilloscope with FFT</li> </ol>	
3	<p><b>ANALOG AND DIGITAL COMMUNICATION TRAINER (BASIC COMMUNICATION SYSTEM)</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* It offers the beginner complete courses of basic analog and digital communication.</li> <li>* Familiarization with the operative aspects of the work in telecommunication laboratory</li> <li>* The building block and components symbols of the circuits are printed on the surface of each modules</li> </ul> <p><b>List of Modules</b></p> <ol style="list-style-type: none"> <li>1. Analog Communication Modules (1) Oscillator/Second Order LPF &amp; HPF (2) AM Modulator/Demodulator (3) DSB-SC and SSB Modulator/Demodulator (4) FM Modulator/Demodulator (5) PLL Frequency Synthesizer (6) TDM and PAM-TDM Multiplexer/Demultiplexer</li> </ol>	

	<p>(7) FDM Multiplexer/Demultiplexer (8) Signal Converter/Recovery/Regeneration</p> <p>2. Digital Communication Modules (1) A/D, D/A Converter Applications (2) PWM Modulator/Demodulator (3) FSK Modulator/Demodulator (4) CVSD Modulator/Demodulator, Manchester Code Encode/Decode (5) ASK Modulator/Demodulator (6) PSK/QPSK Modulator (7) PSK/QPSK Demodulator</p> <p>3. Power Supply and Signal Generator Unit (1) Main Unit</p> <p><b>List of Experiments</b></p> <p><b>Analog Communication</b></p> <ol style="list-style-type: none"> <li>1. RF oscillator experiment</li> <li>2. Second order LPF and HPF experiment</li> <li>3. AM modulator experiment</li> <li>4. AM demodulator experiment</li> <li>5. DSB-SC and SSB modulator experiment</li> <li>6. DSB-SC and SSB demodulator experiment</li> <li>7. FM modulator experiment</li> <li>8. FM demodulator experiment</li> <li>9. Frequency synthesizer</li> <li>10. TDM multiplexer experiments</li> <li>11. TDM demultiplexer experiments</li> <li>12. FDM multiplexer experiments</li> <li>13. FDM demultiplexer experiments</li> <li>14. Frequency converter experiment</li> <li>15. Carrier frequency recovery experiment</li> <li>16. Manchester clock regeneration experiment</li> </ol> <p><b>Digital Communication</b></p> <ol style="list-style-type: none"> <li>1. Analog to digital experiment</li> <li>2. Digital to analog experiment</li> <li>3. PWM modulator experiment</li> <li>4. PWM demodulator experiment</li> <li>5. FSK modulator experiment</li> <li>6. FSK demodulator experiment</li> <li>7. CVSD modulators and demodulators</li> <li>8. Manchester code encode/decode</li> <li>9. ASK modulator/demodulator</li> <li>10. PSK/QPSK modulator/demodulator</li> </ol> <p><b>Accessories:</b></p> <ol style="list-style-type: none"> <li>1. Connector Leads</li> <li>2. Experiment Manual</li> <li>3. Teacher's Guide</li> <li>4. Storage Cabinet x 2</li> <li>5. Rack Frame</li> <li>6. Digital Storage Oscilloscope</li> <li>7. Hand-held Digital Multimeter</li> </ol>	
4	<p><b>ELECTRIC CIRCUIT LAB FUNDAMENTALS</b></p> <p><b>Features:</b></p> <ol style="list-style-type: none"> <li>1. Ideal for electric circuit experiments and design exercises and integrated trainer with complete curriculum</li> <li>2. Complete with power supplies and test systems for easy and efficient experimentation</li> </ol>	

	<p>3. With Universal breadboard (1,680 tie-points) for circuit design and prototyping</p> <p>4. All modules equipped with an 8-bit DIP switch for circuits fault simulations or fault insertion</p> <p><b>Technical Specification:</b></p> <ul style="list-style-type: none"> <li>• Main Unit <ul style="list-style-type: none"> <li>1. DC Power Supply <ul style="list-style-type: none"> <li>1) Fixed DC power supply <ul style="list-style-type: none"> <li>a. Voltage range: : <math>\pm 5\text{DCV}</math> to <math>-5\text{DCV}</math> , <math>+12\text{DCV}</math> to <math>-12\text{DCV}</math></li> <li>b. With output overload protection</li> </ul> </li> <li>2) Dual DC power supply <ul style="list-style-type: none"> <li>a. <math>\pm 3\text{V}</math> to <math>\pm 18\text{DCV}</math>, continuously adjustable</li> <li>b. With output overload protection</li> </ul> </li> </ul> </li> <li>2. AC Power Supply <ul style="list-style-type: none"> <li>1. <math>9\text{ACV}</math> - <math>0\text{ACV}</math> - <math>9\text{ACV}</math></li> <li>2. With output overload protection</li> </ul> </li> <li>3. Function Generator <ul style="list-style-type: none"> <li>Sine, square and triangle</li> <li>10 Hertz to 100 Kilohertz, 4 settings, continuously adjustable</li> <li><math>\pm 5</math> percent of scale value</li> <li>50 Ohms</li> <li><math>\geq 18\text{V}</math> peak to peak (open loop) <math>\geq 9\text{V}</math> peak to peak (with <math>50\Omega</math> load)</li> </ul> </li> <li>4. 3 1/2-Digit Digital Voltmeter /Ammeter <ul style="list-style-type: none"> <li>2DCV to 200DCV</li> <li><math>\pm 0.3\%</math> of reading + 1 digit</li> <li>200<math>\mu\text{A}</math>, to 2000mA</li> <li><math>\pm 0.5\%</math> of reading + 1 digit</li> </ul> </li> <li>5. Analog Meters <ul style="list-style-type: none"> <li>0 to 100mA up to 1A</li> <li>0 to 15V</li> <li>0 to 100mA up to 1A</li> <li>0 to 20V</li> </ul> </li> <li>6. Speaker <ul style="list-style-type: none"> <li>1) <math>8\Omega</math>, 0.25W speaker with driver amplifier circuit</li> </ul> </li> <li>7. Variable Resistors <ul style="list-style-type: none"> <li>1) <math>1\text{K}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>2) <math>10\text{K}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>3) <math>100\text{K}\Omega</math>, 0.25 W variable resistor with 3 terminals (A,B,C)</li> <li>4) <math>1\text{M}\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> </ul> </li> <li>8. Solderless Breadboard <ul style="list-style-type: none"> <li>1) 1,680 tie-points breadboard on top panel can be easily put into and taken off</li> </ul> </li> </ul> <p><b>The Equipment must have the following Experiment Modules:</b></p> <ul style="list-style-type: none"> <li>• Basic Electricity Experiments and Magnetism Element Introduction Module</li> <li>• Magnetic Field, Ampere's Rule, Fleming's Rule Module</li> <li>• Electromagnetic Induction Module</li> </ul> </li></ul>	
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<ul style="list-style-type: none"><li>• Electronic Circuit Fundamental Experiments Module</li><li>• Basic Electronic Circuit Experiments (1 and 2) Module</li><li>• Special Electronic Components Experiments Module</li><li>• Oscillator Experiments and Applications Module</li></ul> <p><b>The package equipment must have the following list of Experiment</b></p> <p><b>1. Experiments for Basic Electricity</b></p> <ul style="list-style-type: none"><li>a. DC voltage measurement, Using an ohmmeter, Resistor characteristics, DC current measurement</li><li>b. AC voltage and AC Current measurement</li><li>c. AC RC, RL, and RLC circuit</li><li>d. Power in AC circuit</li><li>e. Transformer characteristics</li><li>f. Series-resonant and Parallel-resonant circuit</li><li>g. LC filter</li></ul> <p><b>2. Experiments for Magnetism</b></p> <ul style="list-style-type: none"><li>a. Magnetic devices and Magnetic field</li><li>b. Drawing magnetic curves and Magnetic field strength</li><li>c. Lenz's and Faraday's laws</li><li>d. Ampere's rule, Fleming's rule and Self-Induction</li><li>e. Mutual Induction and Magnetic flux detection</li></ul> <p><b>3. Experiments for Basic Electronic Circuits</b></p> <ul style="list-style-type: none"><li>a. Diode characteristics, Rectifier circuit, Filter circuit,</li><li>b. Zener diode, LED, Transistor characteristics</li><li>c. Multimeter functions</li><li>d. FET, SCR, UJT characteristics</li></ul> <p><b>4. Experiments for Simple Electronic Circuits</b></p> <ul style="list-style-type: none"><li>a. Simple, Complementary, Push-pull and Multistage cascading amplifier</li><li>b. Voltage regulator</li><li>c. Wheatstone bridge</li><li>d. Dimmer circuit</li><li>e. Relay characteristics</li><li>f. Touch-controlled switch</li></ul> <p><b>5. Experiments for Industrial Control Applications</b></p> <ul style="list-style-type: none"><li>a. CDS and Thermistor characteristics</li><li>b. Light-controlled, Temperature-controlled and Sound controlled circuit</li></ul> <p><b>6. Experiments for Oscillator Characteristics and Applications</b></p> <ul style="list-style-type: none"><li>a. Blocking oscillator</li><li>b. Electronic birdcall, LC resonant and LED flasher circuit</li><li>c. Astable multivibrator</li></ul> <p><b>The Equipment must package with the following standard accessories:</b></p> <ul style="list-style-type: none"><li>• Experiment manual and instructor's manual</li><li>• Connection leads and plugs: 1 set</li><li>• Inductors: 0.1H, 0.5H each 1 piece</li><li>• Magnet: 1 piece</li><li>• Key (Allen Wrench): 1 piece</li><li>• Handheld Digital Multi Meter</li><li>• Digital Oscilloscope</li></ul>	
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5	<p><b>ELECTRONICS FUNDAMENTALS MODULE</b></p> <p><b>Features:</b></p> <ol style="list-style-type: none"> <li>1. All modules must be equipped with an 8-bit DIP switch for fault insertion/ simulation</li> <li>2. Individual keeping case for easy carrying and Storage.</li> <li>3. With universal breadboard for designing own circuits.</li> <li>4. Uses 2mm connecting plug for the socket</li> </ol> <p><b>Technical Specification:</b></p> <ul style="list-style-type: none"> <li>• Main Unit <ol style="list-style-type: none"> <li>1. DC Power Supply <ol style="list-style-type: none"> <li>1) Fixed DC power supply <ol style="list-style-type: none"> <li>a. Voltage range: +5DCV to - 5DCV, +12DCV to - 12DCV</li> <li>b. With output overload protection</li> </ol> </li> <li>2) Dual DC power supply <ol style="list-style-type: none"> <li>a. <math>\pm 3V</math> to <math>\pm 18</math> DCV, continuously adjustable</li> <li>b. With output overload protection</li> </ol> </li> </ol> </li> <li>2. AC Power Supply <ol style="list-style-type: none"> <li>1. 9ACV - 0ACV - 9ACV</li> <li>2. With output overload protection</li> </ol> </li> <li>3. Function Generator <p>Sine, square and triangle</p> <p>10 Hertz to 100 Kilohertz, 4 settings, continuously adjustable</p> <p><math>\pm 5</math> percent of scale value</p> <p>50 Ohms</p> <p><math>\geq 18V</math> peak to peak (open loop) <math>\geq 9V</math> peak to peak (with <math>50\Omega</math> load)</p> </li> <li>4. 3 1/2-Digit Digital Voltmeter/Ammeter <p>2DCV to 200DCV</p> <p><math>\pm 0.3\%</math> of reading + 1 digit</p> <p>200<math>\mu A</math>, to 2000mA</p> <p><math>\pm 0.5\%</math> of reading + 1 digit</p> </li> <li>5. Analog Meters <p>0 to 100mA up to 1A</p> <p>0 to 15V</p> <p>0 to 100mA up to 1A</p> <p>0 to 20V</p> </li> <li>6. Speaker <ol style="list-style-type: none"> <li>1) 8<math>\Omega</math>, 0.25W speaker with driver amplifier circuit</li> </ol> </li> <li>7. Variable Resistors <ol style="list-style-type: none"> <li>1) 1K<math>\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>2) 10K<math>\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>3) 100K<math>\Omega</math>, 0.25W variable resistor with 3 terminals (A,B,C)</li> <li>4) 1MQ, 0.25W variable resistor with 3 terminals (A,B,C)</li> </ol> </li> <li>8. Solderless Breadboard <ol style="list-style-type: none"> <li>1) 1,680 tie-points breadboard on top panel can be easily put into and taken off</li> </ol> </li> </ol></li></ul>	
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<p><b>The equipment modules shall have the following specification:</b></p> <ol style="list-style-type: none"> <li>1. Each module is secured in a solid-body plastic housing and each module shall equipped with DIP switch (8-bit) for fault insertion/simulations. Students can practice trouble shooting by setting the DIP switch to different positions</li> <li>2. Detailed solutions for the fault simulation are included in the instructor's manual</li> </ol> <p><b>The equipment shall be able to perform the following experiments:</b></p> <ol style="list-style-type: none"> <li>1. Characteristics of Diodes</li> <li>2. Clipping and Clamping Circuits with Diodes</li> <li>3. Rectifier Circuits</li> <li>4. Differential and Integrator Circuits</li> <li>5. Transistors</li> <li>6. Transistor Amplification Circuits</li> <li>7. Field Effect Transistors (FET)</li> <li>8. FET Amplification Circuits</li> <li>9. Multi - Stage Amplification Circuits</li> <li>10. Transistor Negative Feedback Circuits</li> <li>11. Transistor Positive Feedback Circuits</li> <li>12. Regulated Voltage /Constant Current Circuits</li> <li>13. Modulation and Demodulation</li> <li>14. Op Amp</li> <li>15. Basic Characteristics of OP Amplifier</li> <li>16. Basic Characteristics of OP Amplifier (1 and 2) - Negative Feedback</li> <li>17. Basic Characteristics of OP Amplifier - Positive Feedback</li> </ol> <p><b>The set shall come with the following accessories:</b></p> <ol style="list-style-type: none"> <li>1. Experiment manual and instructor's manual</li> <li>2. Connection leads and plugs (2mm): 1 set</li> <li>3. Allen wrench Key: 1 piece</li> <li>4. Handheld Digital Multi Meter</li> <li>5. Digital Oscilloscope</li> </ol> <p><b>The software shall have the following capabilities:</b></p> <ol style="list-style-type: none"> <li>1. Schematic Editor</li> <li>2. Schematic Symbol Editor</li> <li>3. Footprint Editor</li> <li>4. Netlist Editor</li> <li>5. Live 3D Breadboard Tool</li> <li>6. Advanced PCB Designer             <ol style="list-style-type: none"> <li>1) Automatic and Manual Design Tools</li> <li>2) Creating flex PCB's</li> <li>3) Animated 3D view</li> </ol> </li> <li>7. Electrical Rules Check (ERC)</li> <li>8. Interpreter</li> <li>9. Library Manager</li> <li>10. Parameter Extractor</li> <li>11. Text and Equation Editor</li> <li>12. DC analysis</li> <li>13. Transient Analysis</li> <li>14. Fourier analysis</li> <li>15. Digital Simulation</li> <li>16. HDL Simulation (VHDL, Verilog, Verilog-A, Verilog-AMS)</li> </ol>	
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	<p>17. Microcontrollers (MCU) Simulation</p> <p>1) MCU Simulation and Debugging (PIC, AVR, 8051,8085, HCS, ARM)</p> <p>2) Linux and Android Simulation (ARM)</p> <p>3) Mixed Spice Simulation (Berkeley and XSpice)</p> <p>18. Flowchart Editor and Debugger</p> <p>19. AC analysis</p> <p>20. Network analysis</p> <p>21. Noise analysis</p> <p>22. Symbolic analysis</p> <p>23. Monte-Carlo and Worst-case analysis</p> <p>24. Design Tool</p> <p>25. Optimization</p> <p>26. Post-processor</p> <p>27. Presentation</p> <p>28. Interactive mode</p> <p>29. Virtual Instruments</p> <p>1) Digital Multimeter</p> <p>2) Function Generator</p> <p>3) Storage Oscilloscope</p> <p>4) Signal and Network Analyzer</p> <p>5) Digital Signal Generator</p> <p>6) Logic Analyzer</p> <p>30. Real-time Test and Measurements</p> <p>31. Mechatronics Extension</p> <p>1) Sensing and Controlling</p> <p>2) Robotics</p> <p>3) Automotive Electronics</p> <p>32. Faster MCU simulation, real time simulation of delays</p> <p>33. Verilog-AMS: analogue events in digital context</p> <p>34. New format at Fourier analysis: <math>D * \cos(kwt + fi)</math></p> <p>35. The software accepts both M and MEG as mega (1e6), but mega is always displayed as MEG (Spice)</p> <p>36. Macro wizard: Configurable pin order &amp; name at auto generated shapes</p> <p>37. Zoom or Scroll with mouse wheel, as set at View Options</p> <p>38. New features in the HDL editor</p> <p>39. Added reference voltage in the Fourier spectrum dialog</p> <p>40. Added MicroBasic 1st file handling support in the hex/1st debugger</p> <p>41. Multilanguage capability, instant language change for licensed languages</p> <p>42. Open project files with preview of schematics and mechatronics</p> <p>43. EDIF import</p> <p>44. Global Spice variables</p> <p>45. KLU - a faster linear solver and better convergence on large-size or special circuits</p> <p>46. Enhanced and accelerated VHDL and Verilog simulation 10x times faster than in v9</p> <p>47. Xilinx simprim simulation in digital and in mixed mode</p> <p>48. Verilog A</p> <p>49. Verilog AMS</p> <p>50. MCU C compiling on 8051, AVR, PIC16, PIC18, PIC32, ARM</p> <p>51. Spice compatible AD-DA interface</p> <p>52. Oscilloscope works together with the interactive mode</p> <p>53. Frequency and waveform parameters are shown with cursors</p>	
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	54. Hotkeys to AC and Transient Analysis 55. Cursor Max and Min functions added to the Process menu	
6	<p><b>FIBER OPTIC COMMUNICATION TRAINER</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"> <li>* With four different data transmission ways (self-module transmission, module-to-module transmission, PC-to-module transmission, and module-to-PC to-module transmission)</li> <li>* The experiment will show you how easy it is to make productive use of fiber optic materials.</li> <li>* The equipment that you assemble will transmit voice from one point to another, using light traveling through an optical fiber.</li> </ul> <p><b>Specifications:</b></p> <p>Main Unit</p> <ol style="list-style-type: none"> <li>Power: AC-DC Adapter             <ol style="list-style-type: none"> <li>AC input: 100V ~ 240V</li> <li>DC output: 15V, 500mA</li> </ol> </li> <li>Microphone Circuit             <ol style="list-style-type: none"> <li>Frequency range: 20Hz ~ 12KHz</li> <li>With gain 20 amplified circuit</li> </ol> </li> <li>Push-button Switch             <ol style="list-style-type: none"> <li>N.O. Type</li> <li>With LED indication</li> </ol> </li> <li>Function generator             <ol style="list-style-type: none"> <li>Output sine wave with adjustable output amplitude</li> <li>Output square wave, with CMOS level</li> <li>Frequency range: 6Hz ~ 2KHz</li> </ol> </li> <li>Output Speaker             <ol style="list-style-type: none"> <li>8Ω, 1/4W</li> </ol> </li> <li>Transmitter Module             <ol style="list-style-type: none"> <li>Optical fiber light: Red LED, <math>\lambda = 660\text{nm}</math></li> <li>Max. drive current: 50mA</li> <li>Effective coupling micro-lens spotlight</li> <li>Emitter follower</li> </ol> </li> <li>Receiver Module             <ol style="list-style-type: none"> <li>Optical receiving diode                 <ol style="list-style-type: none"> <li><math>\lambda</math> peak: 880nm</li> </ol> </li> <li>Connectable plastic optical fiber with 1,000m core</li> <li>Effective coupling micro lens spotlight</li> <li>Max. consumption power: 100mW</li> <li>With amplified, gain, restoring sharpness circuit</li> </ol> </li> <li>Data transmission elements             <ol style="list-style-type: none"> <li>Chip set: AVR8515, 8bits, 8MHz crystal</li> <li>LCD: back light 20 x 2 letter chip</li> <li>Keyboard: 4 x 4 16Key</li> <li>Character mode: single letter or string letter available</li> <li>Send mode: OFF (self-module transmission), transceiver (module-to-module), PC module PC</li> <li>With reset function</li> <li>Communication interface: RS 232C, 9600 baud rate</li> </ol> </li> </ol>	

	<p>h. Software environment: Windows base</p> <p><b>Experiment Modules</b></p> <ol style="list-style-type: none"><li>1. 2mm connection leads are used throughout the system</li><li>2. The building blocks and components symbols of the circuits are printed on the surface of each module</li><li>3. Modules are secured in plastic housings</li><li>4. Comprehensive experimental manual</li><li>5. Use bridge plugs on circuit loop to reduce the possibility of errors</li></ol> <p><b>List of Experiments</b></p> <ol style="list-style-type: none"><li>1. Characteristic of fiber optics experiment</li><li>2. Applications of fiber optics experiment</li><li>3. Light sources of fiber optics</li><li>4. Light and fiber optics interaction experiment</li><li>5. Fiber optic transmitters experiment</li><li>6. Receivers for fiber optic system experiment</li><li>7. Fiber optic expand and network experiment</li><li>8. Fiber optic connectors and lose-polishing experiment</li><li>9. Fiber optical data-transmission-self-transceiver experiment</li><li>10. Fiber optical data-transmission-double-transceiver experiment</li><li>11. Fiber optical data-transmission - PC module experiment</li><li>12. Fiber optical data-transmission - module PC experiment</li></ol> <p><b>Accessories</b></p> <ol style="list-style-type: none"><li>1. 2mm test lead: 1 set</li><li>2. Plastic fiber optics: 1 set</li><li>3. Experiment manual</li><li>4. RS-232 to USB adapter</li><li>5. Connection plug pitch=10mm</li><li>6. Headphone and microphone</li></ol>	
7	<p><b>INDUSTRIAL ELECTRONICS TRAINER</b></p> <p><b>Features:</b></p> <ul style="list-style-type: none"><li>* Comprehensive study including the theoretical study and practical exercises</li><li>* Use of industrial-type components, devices and circuits</li><li>* The Industrial Electronic Trainer is a self-contained training equipment allowing students to learn more than 70 experiments through a power supply unit and 16 replaceable modules.</li></ul> <p><b>Specification:</b></p> <ol style="list-style-type: none"><li>1. Power Supply Unit<ol style="list-style-type: none"><li>(1) ACV output voltage: 18V-0V-18V, 0.5A</li><li>(2) ACV output voltage: 12V-0V-12V, 0.5A</li><li>(3) DC output voltage: +12V, 0.5A</li><li>(4) DCV output voltage: +5V, 0.5A</li></ol></li><li>2. Meter/Motor Unit<ol style="list-style-type: none"><li>(1) Dual-scale ACV: 150V-300V, class 2.5</li><li>(2) Dual-scale ACA: 0-100mA-1 A, class 2.5</li><li>(3) Dual-scale DCV: 0-10V-20V, class 2.5</li><li>(4) Dual-scale DCA: 0-100mA-1 A, class 2.5</li><li>(5) AC110V/220V motor</li></ol></li></ol>	

<p><b>List of Experiments</b></p> <p><b>1. Power Supply Unit Experiments</b>  (1) AC voltage measurement  (2) DC voltage measurement</p> <p><b>2. UJT Experiments</b>  UJT Characteristic and Equivalent Circuit  (1) UJT introduction  (2) UJT characteristic  (3) UJT equivalent circuit  (4) CDS trigger, RTH trigger</p> <p>UT Oscillator Circuit and Timer Switch  (1) UJT relaxation oscillator  (2) UJT timer switch</p> <p><b>3. PUT Experiments</b>  PUT Characteristic and Equivalent Circuit  (1) PUT introduction  (2) PUT characteristic  (3) PUT equivalent circuit  (4) CDS trigger  (5) RTH trigger  PUT Oscillator Circuit and Timer Switch  (1) PUT circuit oscillator  (2) PUT timer switch</p> <p><b>4. PUT and SCR Experiments</b>  PUT Staircase Generator and Voltage Control Ramp Circuit  (1) PUT staircase generator circuit  (2) PUT voltage control ramp circuit  SCR Characteristic and RC Shift Control Circuit  (1) SCR principle  (2) SCR characteristic curve  (3) SCR construction  (4) SCR trigger mode  (5) SCR RC phase control circuit</p> <p><b>5. SCS Experiments</b>  SCS Characteristic Experiment  (1) SCS construction and operation mode  (2) Use VOM meter measuring SCS  (3) SCS Schmitt circuit  (4) SCS simulate PUT circuit  SCS Trigger Circuit Experiment  (1) CDS trigger  (2) RTH trigger</p> <p><b>6. UJT and PUT Trigger SCR Experiments</b>  UT Trigger SCR Phase Control Circuit  (1) Phase control basic circuit  (2) Phase control analysis  (3) AC phase control circuit analysis  (4) UJT trigger SCR phase control circuit  PUT Trigger SCR Phase Control Circuit</p> <p><b>7. SCR Control DC Motor and DIAC, TRIAC</b>  SCR Characteristic Experiments  SCR Control DC Motor Forward/Reverse Experiment  (1) SCR cut-off principle  (2) SCR control DC motor forward/reverse control experiment</p>	
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	<p>DIAC, TRIAC Characteristic Experiment</p> <ol style="list-style-type: none"> <li>(1) DIAC construction and characteristic</li> <li>(2) DIAC operation mode and measurement</li> <li>(3) TRIAC construction and characteristic</li> <li>(4) TRIAC trigger mode</li> <li>(5) TRIAC static measurement</li> </ol> <p><b>8. Automatic Control Lamp, TRIAC Control Speed Experiments</b></p> <p>Automatic Control Lamp Experiment</p> <ol style="list-style-type: none"> <li>(1) TRIAC shift control</li> <li>(2) TRIAC automatic control lamp experiment</li> </ol> <p>TRIAC Control Motor Speed Experiment</p> <ol style="list-style-type: none"> <li>(1) Different motor Introduction</li> <li>(2) TRIAC control motor speed experiment</li> </ol> <p><b>9. Temperature Ratio, Photo-Couple and Touch Control Experiments</b></p> <p>Bridge Temperature Ratio Control Experiment</p> <ol style="list-style-type: none"> <li>(1) Electronic component of thermal resistor</li> <li>(2) SCR bridge temperature ratio control experiment</li> </ol> <p>Photo-Couple and Touch Control Experiment</p> <ol style="list-style-type: none"> <li>(1) Photo-couple control circuit</li> <li>(2) FET construction and characteristic</li> <li>(3) Touch alarm circuit</li> </ol> <p><b>10. Over/Under Voltage Breaker and Flasher Control Experiments</b></p> <p>Over/Under Voltage Breaker Experiment</p> <ol style="list-style-type: none"> <li>(1) OPA characteristic with reverse &amp; non-reverse circuit</li> <li>(2) Voltage comparison circuit</li> </ol> <p>Flasher Control Experiment</p> <ol style="list-style-type: none"> <li>(1) Application of TRIAC power control</li> <li>(2) AC circuit control</li> <li>(3) Multivibrator</li> </ol> <p><b>11. TRIAC Liquid Level and IC Timer Switch Experiments</b></p> <p>TRIAC Liquid Level Control Experiment</p> <ol style="list-style-type: none"> <li>(1) Digital circuit introduction</li> <li>(2) TRIAC liquid level control experiment</li> </ol> <p>IC Timer Switch Experiment</p> <ol style="list-style-type: none"> <li>(1) NE 555 IC circuit introduction</li> <li>(2) IC timer switch experiment</li> </ol> <p><b>12. Digital Signal Driver and Zero-Voltage Switch Experiments</b></p> <p>Digital Signal Driver Control Experiment</p> <p>Zero-Voltage Switch Experiments (I)</p> <p>Ideal half-wave zero-voltage switch experiments</p> <p><b>13. Zero-Voltage Switch Experiments</b></p> <p>Zero-Voltage Switch Experiments (II)</p> <ol style="list-style-type: none"> <li>(1) TRIAC zero-voltage switch experiments</li> <li>(2) IC mode zero-voltage switch experiments</li> </ol> <p><b>14. SCR Converter Experiments</b></p> <ol style="list-style-type: none"> <li>(1) Parallel converter introduction</li> <li>(2) Series converter introduction</li> <li>(3) Converter trigger source</li> <li>(4) Converter voltage adjustment</li> <li>(5) Converter output-waveform improvement</li> </ol>	
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	<p><b>15. SCR Rectifier Circuit Experiments</b>  (1) Single-phase half-wave rectifier  (2) Single-phase full-wave rectifier  (3) Single-phase bridge rectifier  (4) Three-phase half-wave rectifier  (5) Three-phase full-wave rectifier</p> <p><b>16. JFET/MOSFET Characteristic and MOSFET Speed Control Experiments</b>  (1) JFET characteristic experiment  (2) MOSFET characteristic experiment  (3) MOSFET speed control experiment</p> <p><b>17. IGBT Characteristic and IGBT Speed Control Experiments</b>  (1) IGBT characteristic experiment  (2) IGBT speed control experiment</p> <p><b>18. Accessories (Including Storage Cabinet 2 pieces)</b></p> <p><b>19. Rack Frame</b></p> <p><b>20. Power Supply (0+-30V, 3A)</b></p> <p><b>21. Transformer (For local 3 phase power not 220V)</b></p> <p><b>22. Digital Storage Oscilloscope</b></p> <p><b>23. Hand-held digital multimeter</b></p>	
	<p><b>Terms and Conditions:</b></p> <p>1. Attach proof of ISO certification to ensure that the quality of products and services complies with industry and international standards.  2. Bidder must submit brochure catalog indicating the brand name and model of bid items as additional requirements.  3. Supplier must conduct after sales training for the end-user after the completion of delivery for 18 Engineering faculty members and Laboratory personnel for 5 days.  4. Supplier must provide continuous technical support to equipment parts including services and repair as long as the equipment is functional.  5. Equipment must be supplied with training manuals in English.  6. Please attach Trainor's certification from the manufacturer to assure the quality of training that will be provided.</p> <p><b>After Sales Service:</b></p> <ul style="list-style-type: none"> <li>• Free trainer's training</li> <li>• Free Semi-Annual Re-training with certificate, on request.</li> <li>• Free Consultation/Seminar, on request</li> <li>• Free Annual product inspection, on request</li> <li>• Free Product demonstration, on Request</li> </ul>	
<b>B.</b>	<b>Compliance to the Schedule of Requirements (Section VI)</b>	

I hereby certify to comply and deliver all the above requirements.

Name: \_\_\_\_\_

Legal Capacity: \_\_\_\_\_

Signature: \_\_\_\_\_

Duly authorized to sign the Bid for and behalf of: \_\_\_\_\_

## ***Section VIII. Checklist of Technical and Financial Documents***

### **Notes on the Checklist of Technical and Financial Documents**

The prescribed documents in the checklist are mandatory to be submitted in the Bid, but shall be subject to the following:

- a. GPPB Resolution No. 09-2020 on the efficient procurement measures during a State of Calamity or other similar issuances that shall allow the use of alternate documents in lieu of the mandated requirements; or
- b. Any subsequent GPPB issuances adjusting the documentary requirements after the effectivity of the adoption of the PBDs.

The BAC shall be checking the submitted documents of each Bidder against this checklist to ascertain if they are all present, using a non-discretionary “pass/fail” criterion pursuant to Section 30 of the 2016 revised IRR of RA No. 9184.



# Checklist of Technical and Financial Documents

## I. TECHNICAL COMPONENT ENVELOPE

### *Class “A” Documents*

#### Legal Documents

- ☐ (a) Valid PhilGEPS Registration Certificate (Platinum Membership) (all pages) **in accordance with Section 8.5.2 of the IRR;**

#### Technical Documents

- ☐ (b) Statement of the prospective bidder of all its ongoing government and private contracts, including contracts awarded but not yet started, if any, whether similar or not similar in nature and complexity to the contract to be bid (in a **FORM prescribed by the QC-BAC-GOODS AND SERVICES); and**
- ☐ (c) Statement of the bidder’s Single Largest Completed Contract (SLCC) similar to the contract to be bid, except under conditions provided for in Sections 23.4.1.3 and 23.4.2.4 of the 2016 revised IRR of RA No. 9184, within the relevant period as provided in the Bidding Documents (in a **FORM prescribed by the QC-BAC-GOODS AND SERVICES); and**
- ☐ (d) Original copy of Bid Security. If in the form of a Surety Bond, submit also a certification issued by the Insurance Commission;  
**or**  
Original copy of Notarized Bid Securing Declaration; **and**
- ☐ (e) Conformity with Section VI. (Schedule of Requirements) and Section VII. (Technical Specifications), which may include production/delivery schedule, manpower requirements, and/or after-sales/parts, if applicable; **and**
- ☐ (f) Original duly signed Omnibus Sworn Statement (OSS);  
**and** if applicable, Original Notarized Secretary’s Certificate in case of a corporation, partnership, or cooperative; or Original Special Power of Attorney of all members of the joint venture giving full power and authority to its officer to sign the OSS and do acts to represent the Bidder.

#### Financial Documents

- ☐ (g) The prospective bidder’s computation of Net Financial Contracting Capacity (NFCC) (in a **FORM prescribed by the QC-BAC-GOODS AND SERVICES);**  
**or**  
A committed Line of Credit from a Universal or Commercial Bank in lieu of its NFCC computation.

### *Class “B” Documents*

- ☐ (h) If applicable, a duly signed joint venture agreement (JVA) in case the joint venture is already in existence;  
**or**  
duly notarized statements from all the potential joint venture partners stating that they will enter into and abide by the provisions of the JVA in the instance that the bid is successful.

#### Other documentary requirements under RA No. 9184 (as applicable)

- ☐ (i) *[For foreign bidders claiming by reason of their country’s extension of reciprocal rights to Filipinos]* Certification from the relevant government office of their country stating that Filipinos are allowed to participate in government procurement activities for the same item or product.
- ☐ (j) Certification from the DTI if the Bidder claims preference as a Domestic Bidder or Domestic Entity.

## II. FINANCIAL COMPONENT ENVELOPE

- ☐ (a) Original of duly signed and accomplished Financial Bid Form; **and**
- ☐ (b) Original of duly signed and accomplished Price Schedule(s).

## III. REQUIRED DOCUMENTS in BDS SECTION 20.2 and 21.2

- Attach proof of ISO certification to ensure that the quality of products and services complies with industry and international standards.
- Bidder must submit brochure catalog indicating the brand name and model of bid items.
- Bidder must attach Trainor's certification from the manufacturer to assure the quality of training that will be provided.
- Notarized Affidavit of Undertaking stating compliance to the following:
  - Bidder must conduct after sales training for the end-user after the completion of delivery for 18 Engineering faculty members and Laboratory personnel for 5 days
  - Supplier must provide continuous technical support to equipment parts including services and repair as long as the equipment is functional.
  - Equipment must be supplied with training manuals in English.
- Statement of Warranty – minimum of one (1) year for all items
- Statement of After Sales Service
  - Free trainer's training
  - Free Semi-Annual Re-training with certificate, on request.
  - Free Consultation/Seminar, on request
  - Free Annual product inspection, on request
  - Free Product demonstration, on Request

Note:

1. Please refer to  
[\[https://drive.google.com/file/d/1uiYurh5WrpBL5B\\_pqpzAb62yucAbIR1p/view?usp=sharing\]](https://drive.google.com/file/d/1uiYurh5WrpBL5B_pqpzAb62yucAbIR1p/view?usp=sharing) for the following requirements:
  - a. Computation of NFCC;
  - b. List of All Ongoing Contracts/List of Contracts already awarded but not yet started;
  - c. Statement of Single Largest Completed Contract
2. Please refer to GPPB Resolution No. 16-2020 for the following requirements:
  - a. Bid Form;
  - b. Price Schedule (for Goods Offered from Abroad/ Within the Philippines)
  - c. Bid Securing Declaration; and
  - d. Omnibus Sworn Statement

